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| CLAIMS  |                  |            |                 |           |                   |
|---|------------------|------------|-----------------|-----------|-------------------|
|   | NO. OF<br>CLAIMS |            | EXTRA<br>CLAIMS | RATE      | FEE               |
| Basic Application Fee   |                  |            |                 |           | \$760.00          |
| Total Claims  | 26               | MINUS 20 = | 6               | x \$18.00 | \$108.00          |
| Independent<br>Claims   | 8                | MINUS 3 =  | 5               | x \$78.00 | \$390.00          |
| If multiple dependent claims are presented, add \$260.00  |                  |            |                 |           | 0                 |
| Total Application Fee   |                  |            |                 |           | \$1,258.00        |
| If verified Statement claiming small entity status is enclosed, subtract 50% of Total Application Fee |                  |            |                 |           | 0                 |
| Add Assignment Recording Fee of \$40.00 if Assignment document is enclosed                            |                  |            |                 |           | \$40.00           |
| <b>TOTAL APPLICATION FEE DUE</b>  |                  |            |                 |           | <b>\$1,298.00</b> |

[X] A check in the amount of \$ 1,298.00 is enclosed for the fee due.

[ ] Charge \$ \_\_\_\_\_ to Deposit Account No. 02-4800 for the fee due.

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The Commissioner is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in triplicate.

Respectfully submitted,

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## TITLE OF THE INVENTION

PHOTOMASK, FABRICATION METHOD OF PHOTOMASK, AND  
FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT

## 5 BACKGROUND OF THE INVENTION

## 1. Field of the Invention

10 The present invention relates to a photomask to be used  
in a fabrication process of semiconductor integrated circuits  
such as liquid crystal displays, a fabrication method of the  
photomasks, and a fabrication method for semiconductor  
integrated circuits.

## 2. Description of the Related Art

15 A projection exposure apparatus exposures circuit  
patterns such as semiconductor circuit elements printed on a  
photomask to a wafer. In addition, it is required for the  
projection exposure apparatus to have functions of a higher  
resolution and a fine-pattern reprinted.

20 In general, when a numerical aperture NA is greater and  
the wavelength of exposing light is shorter, the resolution of  
the projection exposure apparatus can be increased. Because the  
depth of focus becomes small during a pattern printing process  
when the numerical aperture NA of the projection exposure  
apparatus is increased, it is limited to increase the numerical  
25 aperture NA. Further, when the wavelength of exposure light is  
changed to smaller wavelength, it must be required to greatly  
change the pattern printing process.

30 In the prior art, an improved method to improve an image  
formation of a photomask has been disclosed. In this improved  
method, a phase shifter to be used for reversing a phase is formed

in a part of a photomask where an exposure light is passed, and a phase of the exposure light is controlled in addition to the control of an amplitude of the exposure light.

For example, there are conventional methods about the  
5 phase shifter described in following patent documents: JP-A-57/62052, JP-A-58/173744, JP-A-62/67514, JP-A-62/67547, JP-A-63/304257, and JP-A-3/263045.

FIG.1A to FIG.1F are diagrams showing a conventional  
fabrication method of a photomask. In FIG.1A to FIG.1F, the  
10 reference number 220 designates a transparent substrate such as a transparent glass substrate, 222 denotes a shade film, and 224 indicates a resist film. When the fabrication process for the photomask is completed, shade patterns 111 are formed on the transparent glass substrate 220 at desired intervals shown  
15 in FIG.1F.

FIG.2A to FIG.2F are diagrams showing a conventional  
fabrication method of a photomask of a low reflection type. In  
FIG.2A to FIG.2F, the reference numbers 231 and 232 designates  
reflection preventing film formed under and on the shade film  
20 222. When the fabrication process for the photomask is completed, the shade patterns 111 are formed at desired intervals on the transparent glass substrate 220 shown in FIG.2F. In this configuration, the reflection preventing films 231 and 232 are formed under and on each shade pattern 111.

FIG.3A to FIG.3D are diagrams showing a conventional  
fabrication method of a phase shift photomask. In FIG.3A to  
FIG.3D, the reference numbers 226 designates phase shift  
sections, and 225 denotes a hollow section (or a cavity  
section).

30 In the final fabrication process to fabricate a

conventional phase shift photomask, as shown in FIG.3D, the shade patterns 111 are formed adjacent to the hollow section 225 formed on the transparent glass substrate 220. Further, the phase shift section 226 are formed adjacent to each shade pattern 111.

As described above, a complicated topography or shape is formed on the photomask fabricated by the conventional photomask fabrication methods such as the method to form the shade patterns on the photomask at desired intervals shown in FIG.1A to FIG.1F and FIG.2A to FIG.2F, and the method to form the phase shift pattern on the photomask at desired intervals shown in FIG.3A to FIG.3D.

Thus, because each conventional photomask fabricated by the conventional methods has a complicated topography, it happens that the shade film is fallen or separated from the transparent glass substrate by a brush to be used during a washing process to eliminate dusts. In other words, the conventional photomask has a lower durability during the photomask fabrication process. This drawback of the conventional photomask causes to decrease a reliability of the photomask and to decrease the yield and quality of semiconductor integrated circuits manufactured by using the conventional photomask.

## SUMMARY OF THE INVENTION

Accordingly, an object of the present invention is, with due consideration to the drawbacks of the conventional technique, to provide photomasks, fabrication methods of the photomasks, and a semiconductor fabrication method of semiconductor integrated circuits such as liquid crystal

displays. The photomasks of the present invention have a high mechanical durability during a fabrication process to fabricate the photomask, and has characteristics of a high image formation, a high quality, a high printing precision, and a higher yield in the fabrication process.

In accordance with a preferred embodiment of the present invention, a photomask comprises a transparent substrate, a hollow section formed on a surface of said transparent substrate, a shade pattern including a shade section, said shade section made up of a shade film and formed in said hollow section, and reflection preventing sections, each formed according to one of cases: each reflection preventing section formed on said shade section; each reflection preventing section formed under said shade section; and each reflection preventing section formed on and under said shade section.

In accordance with another preferred embodiment of the present invention, a photomask comprises a transparent substrate, a shade pattern formed on said surface of said transparent substrate, and a phase shift pattern selectively formed on said shade pattern and said transparent substrate. In the photomask, a surface of said phase shift pattern is flat.

In the photomask as another preferred embodiment, an end section of said phase shift pattern that is contacted to said transparent substrate has a sloped shape that is gradually thin.

In the photomask as another preferred embodiment, a difference of a step between said phase shift pattern and said transparent substrate at said end section of said phase shift pattern that is contacted to said transparent substrate is gradually small.

In accordance with a preferred embodiment of the present invention, a photomask comprises a transparent substrate, a hollow section formed on a surface of said transparent substrate, a shade pattern made up of a shade film, said shade film formed in said hollow section, and a phase shift pattern, whose surface is flat, selectively formed on said transparent substrate having said shade pattern formed in said hollow section.

In the photomask as another preferred embodiment, a thickness of an end section of said phase shift pattern contacted to said transparent substrate is gradually thin.

In accordance with a preferred embodiment of the present invention, a photomask comprises a transparent substrate, a hollow section formed on a surface of said transparent substrate, a shade pattern made up of a shade film, said shade film formed in said hollow section, and a phase shift pattern formed on said transparent substrate including said shade pattern formed in said hollow section.

In the photomask as another preferred embodiment, an end section of said phase shift pattern that is contacted to said transparent substrate has a sloped shape that is gradually thin.

In the photomask as another preferred embodiment, said phase shift pattern includes one of or a combination of: a phase shift pattern of a Levenson's type, a phase shift pattern of an auxiliary shifter type; a phase shift pattern of an edge highlighting type; a phase shift pattern of a half tone type; a phase shift pattern of a half tone type with a shade pattern, a phase shift pattern of a shifter shading type with a shade pattern; and a phase shift pattern of an intermediate phase type.

In accordance with a preferred embodiment of the present

invention, a photomask fabrication method comprises the steps of forming a resist film on a transparent substrate, forming a desired pattern on said resist film by developing said resist film after said resist film is selectively exposing by using  
5 a radiation ray, forming hollow sections in said transparent substrate by selectively etching said transparent substrate by using said resist film as a mask, eliminating said resist film from said transparent substrate, forming a first reflection preventing film in each of said hollow sections, forming a  
10 shade film on said first reflection preventing film formed in each of said hollow sections, forming a shade pattern by performing a chemical and mechanical polishing for said shade film, and forming a second reflection preventing film on said shade pattern. In the photomask fabrication method, said  
15 processes for forming said first reflection preventing process and said second reflection preventing process are performed selectively.

In accordance with a preferred embodiment of the present invention, a photomask fabrication method comprises the steps  
20 of forming a shade pattern on a transparent substrate, coating a phase shift film on both said transparent substrate and said shade pattern, forming a phase shift pattern by selectively etching said phase shift film by using a radiation ray, and performing a chemical and mechanical polishing for a surface  
25 of said phase shift pattern in order to form said phase shift pattern having a desired thickness.

In the photomask fabrication method as another preferred embodiment, after the step of forming said phase shift pattern, further comprises the steps of selectively etching said phase  
30 shift pattern by using said radiation ray, and performing said



chemical and mechanical polishing for said surface of said phase shift pattern that has been etched in order to form said phase shift pattern having a desired thickness and a flat surface.

In the photomask fabrication method as another preferred embodiment, after the step of forming said shade pattern on said transparent substrate, further comprises the steps of forming a resist film on said shade pattern, forming a resist pattern by selectively etching said resist film by using said radiation ray, selectively etching said transparent substrate by using said resist pattern as a mask, eliminating said resist pattern from said transparent substrate, and performing said chemical and mechanical polishing for said surface of said phase shift pattern.

In accordance with a preferred embodiment of the present invention, a photomask fabrication method comprises the steps of forming a resist film on a transparent substrate, forming a desired pattern on said resist film by developing said resist film after said resist film is selectively exposed by using a radiation ray, forming hollow sections in said transparent substrate by selectively etching said transparent substrate by using said resist film as a mask, eliminating said resist film from said transparent substrate, forming a shade film in each of said hollow sections, performing a chemical and mechanical polishing for said shade film in order to form a shade pattern, forming a phase shift film on said transparent substrate, and forming a phase shift pattern by selectively etching said phase shift film by using said radiation ray.

In the photomask fabrication method as another preferred embodiment, after the step of forming said phase shift pattern by selectively etching said phase shift film by using said

radiation ray, further comprises the step of performing said chemical and mechanical polishing for said phase shift pattern formed on said transparent substrate.

In accordance with a preferred embodiment of the present invention, a photomask fabrication method comprising the steps of forming a resist film on a transparent substrate, forming a desired pattern on said resist film by developing said resist film after said resist film is selectively exposed by using a radiation ray, forming hollow sections in said transparent substrate by selectively etching said transparent substrate by using said resist film as a mask, eliminating said resist film from said transparent substrate, forming a shade film in each of said hollow sections, performing a chemical and mechanical polishing for said shade film in order to form a shade pattern, forming a resist film on said transparent substrate in which said shade pattern has been formed, selectively etching said resist film by using said radiation ray, and selectively etching said transparent substrate.

In the photomask fabrication method as another preferred embodiment, after the step of selectively etching said transparent substrate by using said radiation ray, further comprises the step of performing said chemical and mechanical polishing for said transparent substrate.

In the photomask fabrication method as another preferred embodiment, one of an electron beam, a laser beam, and a monochromatic beam is used as said radiation ray.

In accordance with a preferred embodiment of the present invention, a fabrication method of semiconductor integrated circuits such as a liquid crystal display uses said photomask as described above is used.

In accordance with a preferred embodiment of the present invention, a fabrication method of semiconductor integrated circuits such as a liquid crystal display includes said photomask fabrication method according to claim 16 is included.

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#### BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects, features, aspects and advantages of the present invention will become more apparent from the following detailed description of the present invention when taken in conjunction with the accompanying drawings, in which:

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FIG.1A to FIG.1F are diagrams showing a conventional fabrication method of a conventional photomask;

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FIG.2A to FIG.2F are diagrams showing a conventional fabrication method of a conventional low reflecting photomask;

FIG.3A to FIG.3D are diagrams showing a conventional fabrication method of a conventional phase shift photomask;

FIG.4 is a diagram showing a photomask according to the first embodiment of the present invention;

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FIG.5A to FIG.5F are diagrams showing a fabrication method of the photomask according to the first embodiment of the present invention;

FIG.6 is a diagram showing a polishing device to be used for a chemical and mechanical polishing process of a photomask;

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FIG.7A and FIG.7B are diagrams, each showing a washing process for washing a photomask;

FIG.8A to FIG.8F are diagrams showing a fabrication method of the photomask according to the third embodiment of the present invention;

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FIG.9A to FIG.9D are diagrams showing a fabrication

method of the photomask according to the fourth embodiment of the present invention;

FIG.10A to FIG.10D are diagrams showing a fabrication method of the photomask according to the fifth embodiment of the present invention;

FIG.11A to FIG.11E are diagrams showing a fabrication method of the photomask according to the sixth embodiment of the present invention;

FIG.12A to FIG.12D are diagrams showing a fabrication method of the photomask according to the seventh and eighth embodiments of the present invention;

FIG.13A to FIG.13E are diagrams showing a fabrication method of the photomask according to the ninth and tenth embodiments of the present invention;

FIG.14A to FIG.14H are diagrams showing photomasks, each having a phase shift pattern of Levenson's type according to the eleventh embodiment of the present invention;

FIG.15A to FIG.15H are diagrams showing photomasks, each having a phase shift pattern of an auxiliary shifter type according to the twelfth embodiment of the present invention;

FIG.16A to FIG.16H are diagrams showing photomasks, each having the phase shift pattern of an auxiliary shifter type according to the twelfth embodiment of the present invention;

FIG.17A to FIG.17H are diagrams showing photomasks, each having a phase shift pattern of an edge highlighting type according to the thirteenth embodiment of the present invention;

FIG.18A to FIG.18H are diagrams showing photomasks, each having the phase shift pattern of the edge highlighting type according to the thirteenth embodiment of the present

invention;

FIG.19A and FIG.19B are diagrams showing photomasks, each having a phase shift pattern of a half tone type according to the fourteenth embodiment of the present invention;

5        FIG.20A to FIG.20H are diagrams showing photomasks, each having a phase shift pattern of a half tone type with shade patterns according to the fifteenth embodiment of the present invention;

10       FIG.21A to FIG.21H are diagrams showing photomasks, each having a phase shift pattern of a shifter shading type with shade patterns according to the sixteenth embodiment of the present invention;

15       FIG.22A to FIG.22H are diagrams showing the photomasks, each having the phase shift pattern of the shifter shading type with shade patterns according to the sixteenth embodiment of the present invention;

20       FIG.23A to FIG.23H are diagrams showing photomasks, each having a phase shift pattern of an intermediate phase type according to the seventeenth embodiment of the present invention; and

25       FIG.24 is a diagram showing a projection exposure apparatus to be used for the fabrication methods of semiconductor integrated circuits by using the photomasks of the present invention.

#### DESCRIPTION OF THE PREFERRED EMBODIMENTS

30       Other features of this invention will become apparent through the following description of preferred embodiments which are given for illustration of the invention and are not

intended to be limiting thereof.

#### First embodiment

FIG.4 is a diagram showing the photomask according to the first embodiment of the present invention. In FIG.4, the reference number 10 designates a transparent substrate such as a transparent glass substrate and so on, 11 denotes shade patterns formed in hollow sections (or cavity sections) in the transparent glass section 10. In the fabrication process of the photomask having the configuration shown in FIG.4, the shade film made up of a semiconductor material, a chromium Cr or a Molybdenum silicide MoSi is formed in each of the hollow sections. After this process, the chemical and mechanical polishing is performed to form a plane surface made up of the shade patterns 11 and the transparent glass substrate 10.

In the conventional photomask, shown in FIG.1F, formed by the conventional fabrication method, because the shade pattern 111 is formed on the transparent glass substrate 220 and the surface of the photomask has a complicated pattern, there is a drawback that the conventional photomask has a lower mechanical durability during the washing process.

On the other hand, because the photomask according to the first embodiment of the present invention has the flat surface in which the shade patterns 11 are formed on the hollow sections and both the shade patterns 11 and the transparent glass substrate 10 are the same flat surface. Accordingly, the photomask of the first embodiment has no complicated topography and has a higher durability in chemical and mechanical polishing during the washing process.

As described above, according to the photomask of the

first embodiment, because the shade pattern comprising shade films 11 is formed on the hollow sections and the shade patterns 11 and the transparent glass substrate 10 are the same flat surface, the surface of the photomask has no complicate topography, and the photomask is capable of increasing a durability to the chemical and mechanical polishing during the washing process. Therefore it is possible to increase the yield of semiconductor integrated circuits manufactured by using the photomask of the present invention.

#### Second embodiment.

FIG.5A to FIG.5F are diagrams showing the fabrication method of the photomask according to the second embodiment of the present invention. In FIG.5A to 5F, the reference number 21 designates a resist film, 23 denotes a plurality of hollow sections (or cavity sections) formed in the transparent glass substrate 10 at desired intervals, and 24 indicates a shade film. Other components are the same as those of the photomask shown in FIG.4 as the first embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the fabrication method of the photomask as the second embodiment.

The photomask shown in FIG.4 is fabricated by the following fabrication processes.

First, as shown in FIG.5B, the resist film 21 is formed on the transparent glass substrate 10. When a selecting etching is performed, the resist film 21 is exposed by a radiation ray such as an electron beam, a laser beam, a monochromatic beam, and the like. In particular, the material of a resist film to be used for the fabrication process is changed according to

the kind of the radiation ray. It is possible to use a monochromatic beam as the radiation ray during the exposure process. This can be used in the following explanation of the third to seventeenth embodiments according to the present invention.

Next, as shown in FIG.5C, the resist film 21 is selectively exposed by using the radiation ray such as the electron beam, the laser beam, or the monochromatic beam. After this process, the selected resist film 21 is developed by a developing solution, for example, a TMAH solution in order to form the resist pattern 22, shown in FIG.5C.

Next, the selecting etching for the transparent glass substrate 10 is performed by using the resist pattern 22 as a mask. By the selection etching, the plurality of hollow section 23 are formed in the transparent glass substrate 10. After the selecting process, the shade film 24 made up of the material of a Chromium Cr or a Molybdenum silicide MoSi or others is formed on the transparent glass substrate 10 having the hollowing sections 23. Then, the chemical and mechanical polishing to the shade film 24 is so performed that both the transparent glass substrate 10 and the shade patterns 11 are the same surface. The fabrication process of the photomask is thereby completed.

Next, a description will be given of the chemical and mechanical polishing method to polish the shade film 24 formed on the transparent glass substrate 10 shown in FIG.5E.

FIG.6 is a diagram showing a polishing device to be used during the chemical and mechanical polishing processes for polishing the shade film 24 in the photomask. In FIG.6, the reference number 31 designates a rotation table, 32 denotes each mask holder for holding a photomask located on the rotation



table 31. In FIG.6, the four mask holders are placed on the rotation table 31. Each mask holders 32 rotates to a different direction when comparing with the direction of the rotation of the rotation table 31. The reference number 33 designates a polishing cloth on the rotation table 31. On the polishing cloth 33, there is a slurry including fine grains such as alumina (Aluminium oxide  $Al_2O_3$ ) and silica (Silicon dioxide  $SiO_2$ ).

Each transparent glass substrate 10 shown in FIG.5E is placed on each rotation table 31 so that the surface on which the shade film 24 is formed, namely a pattern surface of the transparent glass substrate 10, is faced to the polishing cloth 33. After the setting of the transparent glass substrate 31, the rotation table 31 is rotated to the direction designated by the arrows shown in FIG.6. This rotation performs the polishing process. The degree of the polishing is adjusted by the pressure of each mask holder 32 to the rotation table 31, and the number of the rotation speed of each of the rotation table 31 and the mask holder 32. After the completion of the polishing process, both the shade sections 11 and the transparent glass substrate 10 have the same flat surface shown in FIG.5F. The surface of the photomask becomes flat.

FIG.7A is a diagram showing a washing process for washing a conventional photomask and FIG.7B is a diagram showing the washing process for washing the photomask of the second embodiment shown in FIG.5F. In FIG.7A and FIG.7B, the reference number 41 designates a blush, and 42 denotes a dust on the photomask to be eliminated during the washing process.

As shown in FIG.7A, the photomask formed by the conventional fabrication process has a complicated topography in which the shade sections 111 are on the transparent glass

substrate 220. In this configuration of the conventional photomask, the shade sections 111 are eliminated from the transparent glass substrate 220 by the mechanical stress such as the brush during the washing process. This causes pattern defects when the photomask in which one or more shade sections 111 are eliminated is exposed during the exposure process. That is, the quality of the photomask is decreased and the quality of semiconductor integrated circuits such as liquid crystal displays and so on manufactured by using the conventional photomask is also decreased. This causes to decrease the yield of the semiconductor integrated circuits.

On the other hand, as shown in FIG.7B, because the photomask formed by using the fabrication process of the second embodiment has the flat surface, there is no mechanical stress such as the blush 41 during the washing process. This configuration of the photomask of the present invention can achieve a high quality. Further, the quality of the semiconductor integrated circuits such as liquid crystal displays and liquid crystal panels can be increased. This causes to increase the yield of the semiconductor integrated circuits.

As described above, according to the second embodiment, because it is possible to form the photomask having the flat surface, the quality of the photomask becomes high and it is also possible to increase the quality of semiconductor integrated circuits such as liquid crystal displays and liquid crystal panels manufactured by using the photomask. In addition to those features, it is possible to increase the yield of the semiconductor integrated circuits.

Third embodiment.

FIG.8A to FIG.8F are diagrams showing a fabrication method of the photomask according to the third embodiment of the present invention. In FIG.8A to FIG.8F, the reference number 50 designates a first reflection preventing film, and 51 denotes a second reflection preventing film. Other components are the same as those of the second embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the fabrication method of the photomask as the third embodiment.

By using the fabrication method for the photomask of the third embodiment shown in FIG.8A to FIG.8D, the transparent glass substrate 10 is processed to form the hollow sections 23 (or cavity sections) on the transparent glass substrate 10 (see FIG.8A to FIG.8D). Following this, as shown in FIG.8D, the first reflection preventing film 50 is formed on the hollow sections 23 and the transparent glass substrate 10, in particular, so that the first reflection preventing film 50 is not buried completely in each of the hollow sections 23 (see the first reflection film 50 shown in FIG.8E). The shade films made up of a semiconductor material Cr or MoSi are then formed on the first reflection preventing film 50 in the hollow sections 23.

After this process, the chemical and mechanical polishing to the shade film 24 is performed to form a plane surface made up of the shade patterns 11 and the transparent glass substrate 10. That is, both the shade patterns 11 and the transparent glass substrate 10 have the same surface. Then, as shown in FIG.8F, the second reflection preventing film 51 is formed on the shade patterns 11 and the transparent glass substrate 10.

The material of the second reflection preventing film 51 is a magnesium fluoride  $MgF_2$ , or a chromium monoxide  $CrO$ , and

so on. In the configuration of the photomask shown in FIG.8, both the first and second reflection preventing films 50 and 51 are formed. However, the present invention is not limited by this configuration, for example, it is acceptable to form one of them on or under the shade pattern 11.

Because the conventional photomask of a low reflection type shown in FIG.2F has a multi-layer configuration, it happens that the shade sections 111 are fallen or separated from the transparent glass substrate 220 by a brush to be used during a washing process to eliminate dusts. In other words, the conventional photomask has a lower durability during the photomask fabrication process. This drawback of the conventional photomask shown in FIG.2F causes to decrease the quality of the photomask and to decrease the yield and quality of semiconductor integrated circuits manufactured by using the conventional photomask. On the other hand, the photomask of the third embodiment shown in FIG.8F has the configuration in which the shade pattern 11 is formed in the transparent glass substrate 10, so that the photomask of the third embodiment has the large mechanical strength. Thus, the configuration of the photomask of the third embodiment contributes to decrease the occurrence of a structural defect of the shade pattern.

As described above, according to the third embodiment, the shade pattern 11 is formed on the transparent glass substrate 10 and the surface of the photomask is flat and the strength of the photomask to the mechanical stress becomes large. It is therefore possible to decrease the occurrence of the fault of the shade pattern 11 and to form the photomask with a high quality. Further, it is possible to increase the quality of semiconductor integrated circuits such as liquid crystal

displays and liquid crystal panels manufactured by using this photomask. In addition to those features, it is possible to increase the yield of the semiconductor integrated circuits.

5 Fourth embodiment.

FIG.9A to FIG.9D are diagrams showing a fabrication method of the phase shift photomask according to the fourth embodiment of the present invention. In FIG.9A to 9D, the reference number 60 designates a phase shift film coated with a shifter material. The reference number 61 denotes a shade pattern, and 62 designates a phase shift pattern. Other components are the same as those of the second embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the fabrication method of the photomask having the phase shift pattern as the fourth embodiment.

First, as shown in FIG.9A, the shade pattern 61 is formed on the transparent glass substrate 10. After this process, the shifter material such as SOG (Spin On Glass) is coated on both the transparent glass substrate 10 and the shade pattern 61. The configuration of the phase shift film 60 formed by coating the shifter material has a waved-shape shown in FIG.9B because the shifter material 60 is coated on a complicated topography of the shade pattern 61.

Next, the chemical and mechanical polishing is performed for the phase shift film 60 in order to obtain a flat surface of the phase shift film 60. After the completion of the polishing process, the phase shift film 60 is selectively etched (namely, a selection etching is performed for the phase shift film 60) in order to form the phase shift mask having the phase shift

pattern 62 with a uniform thickness of each shifter shown in FIG.9C.

As shown in FIG.3C, because the thickness of each shifter becomes partially high, namely, the thickness of each shifter does not uniform, the conventional phase shift photomask does not perform completely its function. On the other hand, the phase shift photomask fabricated by the fabrication method of the fourth embodiment can perform completely its function because the thickness of the shifters forming the phase shift pattern 62 is uniform.

As described above, because the fabrication method of the fourth embodiment can fabricate the phase shift pattern of a uniform thickness, it is possible to increase the contrast of an optical image of the phase shift photomask when semiconductor integrated circuits are fabricated by using the phase shift photomask.

#### Fifth embodiment.

FIG.10A to FIG.10D are diagrams showing a fabrication method of the photomask according to the fifth embodiment of the present invention. In FIG.10A to FIG.10D, the reference number 63 designates a phase shift pattern, and 70 denotes a boundary section between the phase shift pattern and the transparent glass substrate 10. Other components are the same as those of the fourth embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the fabrication method of the phase shift photomask of the fifth embodiment as a photomask.

First, as shown in FIG.10A and FIG.10B, the shade pattern

61 is formed on the transparent glass substrate 10. Next, the phase shift film 60 is formed on both the shade pattern 61 and the transparent glass substrate 10 by using the fabrication process of the fourth embodiment shown in FIG.9A and FIG.9B.

5       Next, as shown in FIG.10C, after the phase shift film 60 is selectively etched, the chemical and mechanical polishing is performed so that the surface of the phase shift film 60 as the coated film becomes flat. Thereby, the photomask in which the surface of the phase shift pattern 63 becomes flat can be  
10       obtained. No phase error happens during the fabrication process for semiconductor integrated circuits by using the photomask obtained by the above processes.

15       In addition, as shown in the right section in FIG.10D, the boundary section 70 in the phase shift section adjacent to the shade pattern 61 has a sloped shape where the phase is gradually changed. Thus, by performing the chemical and mechanical polishing for the boundary section 70, an intermediate phase shifter can be formed in the boundary section 70. That is, the boundary section 70 becomes a faulty section  
20       of the phase shifter if no chemical and mechanical polishing is performed. However, no shade section is formed at the boundary section 70 by performing the chemical and mechanical polishing. Furthermore, as shown in FIG.10D, no mechanical stress by the brush 41 (see FIG.7A) happens during the washing  
25       process because the surface of the photomask has a unique topography, namely flat. This means the photomask of the fifth embodiment has a high durability during the washing process.

30       As described above, according to the fifth embodiment, it is possible to avoid the occurrence of the faulty phase shifter at the boundary section 70 in the photomask and to form

the phase shift photomask having the configuration in which no shade section are formed at the boundary section 70. Therefore the topography of the surface of the photomask becomes flat. It is thereby possible to form the photomask having a high durability to the washing process.

Sixth embodiment.

FIG.11A to FIG.11E are diagrams showing the fabrication method of the photomask according to the sixth embodiment of the present invention. In FIG.11A to 11E, the reference number 80 designates a resist film, 82 denotes a phase shift pattern, and 83 indicates a hollow section (or a cavity section). Other components are the same as those of the fifth embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the fabrication method of the photomask having the phase shift pattern as the sixth embodiment.

First, as shown in FIG.11A, the shade pattern 61 is formed on the transparent glass substrate 10. Next, the resist film 80 is formed on both the shade pattern 61 and the transparent glass substrate 10. The resist film 80 has a complicated topography such as a convex shape because the shade pattern 61 is formed on the transparent glass substrate 10.

Next, as shown in FIG.11C and FIG.11D, the transparent glass substrate 10 is selectively etched by using the resist film 80 and the shade pattern 61 as a mask in order to form the hollow section 83 in the transparent glass section 10. In this configuration, the phase shift pattern 82 is formed at the section adjacent to the shade pattern 61.

Next, the chemical and mechanical polishing is performed,



so that the configuration of the boundary section 70 between the phase shift pattern 82 (see FIG.11D) and the transparent glass substrate 10 (see FIG.11D) becomes a sloped shape, as shown in FIG.11E. In the boundary section 70 of the sloped shape, the phase is gradually changed. By performing the chemical and mechanical polishing for the boundary section 70, an intermediate phase shifter can be automatically formed in the boundary section 70. That is, the boundary section 70 becomes a faulty section of the phase shifter if no chemical and mechanical polishing is performed. However, no shade section is formed at the boundary section 70 by performing the chemical and mechanical polishing for the boundary section 70. Further, as shown in FIG.11E, because the topography of the parts, where no phase shift section is formed, in the photomask fabricated by the fabrication method of the sixth embodiment is more flat when comparing with the conventional one shown in FIG.3D, no mechanical stress by the brush 41 (see FIG.7A) happens during the washing process. This means the photomask of the sixth embodiment has a high durability during the washing process.

As described above, according to the sixth embodiment, it is possible to avoid the occurrence to generate the faulty phase shifter at the boundary section 70, and it is possible to form the phase shift photomask having the configuration in which no shade section is formed at the boundary section 70 by performing the chemical and mechanical polishing. Therefore the topography of the surface of the photomask becomes flat when comparing with the conventional one. It is thereby possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process.

Seventh embodiment.

FIG.12A and FIG.12B are diagrams showing a fabrication method of the photomask according to the seventh embodiment of the present invention. In FIG.12A and FIG.12B, the reference number 63 designates a phase shift pattern. Other components are the same as those of the fifth embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the fabrication method of the photomask having the phase shift pattern as the seventh embodiment.

First, as shown in FIG.12A, the shade pattern 11 is formed in the hollow section 11 (or the cavity section) formed in the transparent glass substrate 10 by the fabrication method of the second embodiment shown in FIG.5A to FIG.5F.

Next, the phase shift film 60 made up of a shifter material such as SOG (Spin on glass) is coated on both the transparent glass substrate 10 and the shade pattern 11. The surface of the phase shift film 60 formed by coating the shifter material becomes flat, as shown in FIG.12B, because the surface of the transparent glass substrate 10 is flat.

Next, the chemical and mechanical polishing is performed for the phase shift film 60 in order to obtain the phase shift pattern 63 of a flat surface and a unique thickness.

As described above, according to the seventh embodiment, because the photomask having the phase shift pattern 63 can be formed on the photomask having the configuration of the first embodiment, it is possible to increase the contrast of an optical image of the phase shift photomask when semiconductor integrated circuits are fabricated by using the phase shift

photomask of the seventh embodiment.

Eighth embodiment.

FIG.12D is a diagram showing a fabrication method of the  
5 photomask according to the eighth embodiments of the present  
invention. In FIG.12D, the reference number 70 designates a  
boundary section between the transparent glass substrate 10 and  
the phase shift pattern 63. Other components are the same as  
those of the seventh embodiment, therefore the explanation of  
10 them is omitted here.

Next, a description will be given of the operation of the  
fabrication method of the photomask having the phase shift  
pattern as the eighth embodiment.

After the fabrication process of the seventh embodiment  
15 shown in FIG.12A to FIG.12C, the chemical and mechanical  
polishing is further performed for the photomask shown in  
FIG.12C, so that the configuration of the boundary section 70  
between the phase shift pattern 63 (see FIG.12C) and the  
transparent glass substrate 10 (see FIG.12C) becomes a sloped  
20 shape, as shown in FIG.12D. In the boundary section 70 of the  
sloped shape, the phase is gradually changed. By performing the  
chemical and mechanical polishing, an intermediate phase  
shifter can be automatically formed in the boundary section 70.  
That is, the boundary section 70 becomes a faulty section of  
25 the phase shifter if no chemical and mechanical polishing is  
performed. However, no shade section is formed at the boundary  
section 70 by performing the chemical and mechanical polishing.  
Further, as shown in FIG.12D, because the topography of the  
photomask is more flat when comparing with the conventional one  
30 shown in FIG.3D, no mechanical stress by the brush 41 (see

FIG.7A) happens during the washing process. This means the photomask of the eighth embodiment has a high durability during the washing process.

As described above, according to the eighth embodiment,  
5 because the photomask with the phase shift pattern can be formed on the configuration of the photomask of the first embodiment, it is possible to increase the contrast of an optical image when semiconductor integrated circuits are manufactured by using the photomask of the eighth embodiment. In addition, it is possible  
10 to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of the photomask is flat when comparing with the conventional one.

15 Ninth embodiment.

FIG.13A to FIG.13D are diagrams showing the fabrication method of the photomask according to the ninth embodiment of the present invention. In FIG.13A to FIG.13D, the reference number 100 designates a resist film, 101 denotes a resist  
20 pattern, 102 indicates a phase shift pattern, and 103 designates a hollow section (or a cavity section). Other components are the same as those of the sixth embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the  
25 fabrication method of the photomask having the phase shift pattern as the ninth embodiment.

First, the photomask having a flat surface including the shade pattern 11 formed in the hollow section shown in FIG.13A is fabricated by using the fabrication method of the second  
30 embodiment shown in FIG.5A to FIG.5F.

Next, as shown in FIG.13B, the resist film 100 is coated on the transparent glass substrate 10 in which the shade pattern 11 is formed. Because the photomask fabricated by the fabrication method of the present invention includes the shade pattern 11 formed in the hollow section and the surface of the photomask is flat, the surface of the resist film 100 becomes flat.

On the other hand, because the shade pattern 111 formed on the transparent glass substrate 220 in the conventional photomask fabricated by the conventional fabrication process has a complicated topography, the surface of the resist film 224 has also a complicated shape.

Next, when the resist film 100 obtained by the fabrication process shown in FIG.13B is developed in order to form the resist pattern 101 shown in FIG.13C. Then, the transparent glass substrate 10 is selectively etched by using the resist pattern 101 and the shade pattern 11 as a mask, so that the phase shift pattern 102 having the hollow section 103 shown in FIG.13D is formed.

As described above, according to the ninth embodiment, because it is possible to form the photomask having the phase shift pattern on the configuration of the photomask as the first embodiment, it is possible to increase the contrast of an optical image when semiconductor integrated circuits are manufactured by using the photomask of the ninth embodiment. In addition, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of the photomask is flat when comparing with the conventional one.

Tenth embodiment.

FIG.13E is a diagram showing a fabrication method of the photomask according to the tenth embodiments of the present invention. In FIG.13E, the reference number 70 designates a boundary section between the transparent glass substrate 10 and the phase shift pattern 102. Other components are the same as those of the ninth embodiment, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the fabrication method of the photomask having the phase shift pattern as the tenth embodiment.

After the fabrication process of the ninth embodiment shown in FIG.13A to FIG.13D, the chemical and mechanical polishing is further performed for the photomask shown in FIG.13D, so that the configuration of the boundary section 70 between the phase shift pattern 102 (see FIG.13D) and the transparent glass substrate 10 (see FIG.13D) becomes a sloped shape, as shown in FIG.13E. In the boundary section 70 of the sloped shape, the phase is gradually changed. By performing the chemical and mechanical polishing for the boundary section 70, an intermediate phase shifter can be automatically formed in the boundary section 70. That is, the boundary section 70 becomes a faulty section of the phase shifter if no chemical and mechanical polishing is performed. However, no shade section is formed at the boundary section 70 by performing the chemical and mechanical polishing for the boundary section 70. In addition, as shown in FIG.13E, because the topography of the photomask is more flat when comparing with the conventional one shown in FIG.3D, no mechanical stress by the brush 41 (see

FIG.7A) happens during the washing process. This means the photomask of the tenth embodiment has a high durability during the washing process.

As described above, according to the tenth embodiment,  
5 because the photomask with the phase shift pattern can be formed on the configuration of the photomask of the ninth embodiment, it is possible to increase the contrast of an optical image when semiconductor integrated circuits are manufactured by using the photomask of the tenth embodiment. In addition, it is possible  
10 to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of the photomask is flat when comparing with the conventional one.

15 **Eleventh embodiment.**

FIG.14A to FIG.14H are diagrams showing photomasks, each has a phase shift pattern of Levenson's type. In particular, FIG.14B to FIG.14D and FIG.14F show the photomasks having the phase shift pattern of Levenson's type that can be fabricated  
20 only by using the fabrication methods of the second embodiment to the tenth embodiment of the present invention. In addition, FIG.14A, FIG.14E, FIG.14G, and FIG.14H show the photomasks of Levenson's type fabricated by using the conventional fabrication methods. In the eleventh embodiment, each component  
25 is the same as each component shown in the first to tenth embodiments, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the photomasks having the phase shift pattern of Levenson's type as the eleventh embodiment.

30 In the photomask having a phase shift pattern, the

principle of an image formation of a radiation ray and the effect thereof are generally changed according to the arrangement of a phase shift section where the phase shift pattern is formed, a shade section where the shade pattern is formed, and a transparent section through which the radiation ray is passed.

When comparing with the conventional photomask having the phase shift pattern of a Levenson's type, the structure of the photomask having the phase shift pattern of the Levenson's type as the eleventh embodiment shown in FIG.14B to FIG.14D and FIG.14F has the unique thickness of the phase shifter and the surface of the photomask is flat. Therefore the topography of the surface of the photomask shown in each of FIG.14B to FIG.14D and FIG.14F is more flat when comparing with that of the photomask shown in each of FIG.14A, FIG.14E, FIG.14G, and FIG.14G. Thereby, the function of the phase control can be increased and the contrast of the optical image can also be increased. In addition to this features, no mechanical stress happens by the brush 41 (see FIG.7A) during the washing process. This means the photomasks of the eleventh embodiment shown in FIG.14B to FIG.14D and FIG.14F has a high durability.

As described above, according to the eleventh embodiment, because the topography of the surface of the photomask is flat and the difference of the step at the shifter section is smaller in height, the effect of a wave guide is relaxed, and the difference between the shifter section and the transparent section is decreased. For example, when semiconductor integrated circuits are manufactured by using the photomasks of the eleventh embodiment, it is possible to increase the performance of the image formation. In addition, it is possible to form the photomask having a high durability to the mechanical



stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of the photomask is flat when comparing with the conventional photomask, as shown in FIG.14A, FIG.14E, and FIG.14G.

5

Twelfth embodiment.

FIG.15A to FIG.15H and FIG.16A to FIG.16H are diagrams showing photomasks, each has a phase shift pattern of an auxiliary shifter type. In particular, FIG.15B to FIG.15D, 10 FIG.15F, FIG.16B to FIG.16D, and FIG.16F show the photomasks having the shift pattern of auxiliary shifter type that can be formed only by the fabrication methods of the second to tenth embodiments according to the present invention. The photomasks having the phase shift pattern of an auxiliary shifter type 15 shown in FIG.15A, FIG.15E, FIG.15G, FIG.15H, FIG.16A, FIG.16E, FIG.16G, and FIG.16H are formed by using the conventional photomask fabrication methods. Other components are the same as those of the first to tenth embodiments, therefore the explanation of them is omitted here.

20

Next, a description will be given of the operation of the photomasks having the phase shift pattern of the auxiliary shifter type as the twelfth embodiment.

25

When comparing with the conventional photomasks having the phase shift pattern of the auxiliary shifter type, each of the photomasks having the phase shift pattern of the auxiliary shifter type shown in FIG.15B to FIG.15D, FIG.15F, FIG.16B to FIG.16D, and FIG.16F has a unique thickness of the phase shifter and the surface of each photomask is flat. Thereby, the topography of the surface of each photomask is more flat when 30 comparing with that of each photomask shown in FIG.15A, FIG.15E,

FIG.15G, FIG.15H, FIG.16A, FIG.16E, FIG.16G, and FIG.16H. Thereby, the function of the phase control can be increased and the contrast of the optical image can also be increased. In addition to this features, no mechanical stress happens by the brush 41 (see FIG.7A) during the washing process. This means each of the photomasks of the twelfth embodiment has a high durability.

As described above, according to the twelfth embodiment, because the topography of the surface of each photomask is flat, the phase control can be easily performed, so that the contrast of an optical image can be increased. That is, as shown in FIG.15B to FIG.15D, FIG.15F, FIG.16B to FIG.16D, and FIG.16F, because the difference of the step at the shifter section is smaller in height, the effect of a wave guide is reduced, and the difference between the shifter section and the transparent section is decreased. For example, when semiconductor integrated circuits are manufactured by using the photomasks of the twelfth embodiment, it is possible to increase the performance of the image formation. In addition, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of each photomask is flat when comparing with each conventional photomask, as shown in FIG.15A, FIG.15E, FIG.15G, FIG.15H, FIG.16A, FIG.16E, FIG.16G, and FIG.16H.

Thirteenth embodiment.

FIG.17A to FIG.17H and FIG.18A to FIG.18H are diagrams showing photomasks, each has a phase shift pattern of an edge highlighting type. In particular, FIG.17B to FIG.17D, FIG.17F,

FIG.18B to FIG.18D, and FIG.18F show the photomasks having the shift pattern of the edge highlighting type that can be formed only by the fabrication methods of the second to tenth embodiments according to the present invention. The photomasks having the phase shift pattern of the edge highlighting type shown in FIG.17A, FIG.17E, FIG.17G, FIG.18H, FIG.18A, FIG.18E, FIG.18G, and FIG.18H are formed by using the conventional photomask fabrication methods. Other components are the same as those of the first to tenth embodiments, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the photomasks having the phase shift pattern of the edge highlighting type as the thirteenth embodiment.

When comparing with the conventional photomasks having the phase shift pattern of the edge highlighting type, each of the photomasks having the phase shift pattern of the edge highlighting type shown in FIG.17B to FIG.17D, FIG.17F, FIG.18B to FIG.18D, and FIG.18F has a unique thickness of the phase shifter and the surface of each photomask is flat. Thereby, the topography of the surface of each photomask is more flat when comparing with that of each photomask shown in FIG.17A, FIG.17E, FIG.17G, FIG.18H, FIG.18A, FIG.18E, FIG.18G, and FIG.18H. Thereby, the function of the phase control can be increased and the contrast of the optical image can also be increased. In addition to this features, no mechanical stress happens by the brush 41 (see FIG.7A) during the washing process. This means each of the photomasks of the thirteenth embodiment has a high durability.

As described above, according to the thirteenth embodiment, because the topography of the surface of each

photomask is flat, the phase control can be easily performed, so that the contrast of an optical image can be increased. That is, as shown in FIG.17B to FIG.17D, FIG.17F, FIG.18B to FIG.18D, and FIG.186F, because the difference of the step at the shifter section is smaller in height, the effect of a wave guide is reduced, and the difference between the shifter section and the transparent section is decreased. For example, when semiconductor integrated circuits are manufactured by using the photomasks of the thirteenth embodiment, it is possible to increase the performance of the image formation. In addition, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of each photomask is flat when comparing with each photomask shown in FIG.17A, FIG.17E, FIG.17G, FIG.17H, FIG.18A, FIG.18E, FIG.18G, and FIG.18H.

#### Fourteenth embodiment.

FIG.19A and FIG.19B are diagrams showing photomasks, each has a phase shift pattern of a half tone type. In particular, FIG.19B shows the photomask having the shift pattern of the half tone type that can be formed only by the fabrication method of the second embodiment according to the present invention. The photomask having the phase shift pattern of the half tone type shown in FIG.19A is formed by using the conventional photomask fabrication method. Other components are the same as those of the first to tenth embodiments, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the photomask having the shift pattern of the half tone type as the

fourteenth embodiment.

When comparing with the conventional photomask having the phase shift pattern of the half tone type, the photomask having the phase shift pattern of the half tone type shown in FIG.19B has the configuration in which the surface of the photomask is flat. Thereby, the topography of the surface of each photomask is more flat when comparing with that of the photomask shown in FIG.19A. Thereby, no mechanical stress happens by the brush 41 (see FIG.7A) during the washing process. This means each of the photomasks of the fourteenth embodiment has a high durability. The semiconductor material such as MoSiON, or CrON is used for forming the half tone section in the photomask.

As described above, according to the fourteenth embodiment, because the topography of the surface of the photomask is flat, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process when comparing with the photomask having the configuration shown in FIG.19A.

Fifteenth embodiment.

FIG.20A to FIG.20H are diagrams showing photomasks having a phase shift pattern of the half tone type with shade patterns according to the fifteenth embodiment of the present invention. In particular, FIG.20B to FIG.20D, and FIG.20F show the photomasks having the shift pattern of the half tone type with the shade pattern that can be formed only by the fabrication methods of the second to tenth embodiments according to the present invention. The photomasks having the phase shift pattern of the half tone type with the shade pattern shown in FIG.20A, FIG.20E, FIG.20G, and FIG.20H are formed by using the

conventional photomask fabrication methods. Other components are the same as those of the first to tenth embodiments, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the photomask having the phase shift pattern of the half tone type with the shade pattern as the fifteenth embodiment.

When comparing with the conventional photomasks having the phase shift pattern of the half tone type with the shade pattern, in each of the photomasks having the phase shift pattern of the half tone type with the shade pattern shown in FIG.20B to FIG.20D, and FIG.20F, the surface of each photomask is flat. Thereby, the topography of the surface of each photomask is more flat when comparing with that of each photomask shown in FIG.20A, FIG.20E, FIG.20G, and FIG.20H. Thereby, no mechanical stress happens by the brush 41 (see FIG.7A) during the washing process. This means each of the photomasks of the fifteenth embodiment has a high durability.

As described above, according to the fifteenth embodiment, because the topography of the surface of each photomask is flat, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process.

Sixteenth embodiment.

FIG.21A to FIG.21H and FIG.22A to FIG.22H are diagrams showing a photomask, each having a phase shift pattern of a shifter shading type with shade patterns. In particular, FIG.21B to FIG.21D, FIG.21F, FIG.22B to FIG.22D, and FIG.22F show the photomasks having the shift pattern of the shifter shading type with the shade pattern that can be formed only by

the fabrication methods of the second to tenth embodiments according to the present invention. The photomasks having the phase shift pattern of the shifter shading type with the shade pattern shown in FIG.21A, FIG.21E, FIG.21G, FIG.21H, FIG.22A, FIG.22E, FIG.22G, and FIG.22H are formed by using the conventional photomask fabrication methods. Other components are the same as those of the first to tenth embodiments, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the photomasks having the phase shift pattern of the shifter shading type with the shade pattern as the sixteenth embodiment.

When comparing with the conventional photomasks having the phase shift pattern of the shifter shading type with the shade pattern, in each of the photomasks having the phase shift pattern of the edge highlighting type shown in FIG.21B to FIG.21D, FIG.21F, FIG.22B to FIG.22D, and FIG.22F, the surface of each photomask is flat and each shifter in the photomask has an unique thickness. Thereby, the topography of the surface of each photomask is more flat when comparing with that of each photomask shown in FIG.21A, FIG.21E, FIG.21G, FIG.21H, FIG.22A, FIG.22E, FIG.22G, and FIG.22H. Thereby, the function of the phase control can be increased and the contrast of the optical image can also be increased. In addition to this features, no mechanical stress happens by the brush 41 (see FIG.7A) during the washing process. This means each of the photomasks of the sixteenth embodiment has a high durability.

As described above, according to the sixteenth embodiment, because the topography of the surface of each photomask is flat, the phase control can be easily performed, so that the contrast of an optical image can be increased. That is, as shown in FIG.21B

to FIG.21D, FIG.21F, FIG.22B to FIG.22D, and FIG.22F, because the difference of the step at the shifter section is smaller in height, the effect of a wave guide is reduced, and the difference between the shifter section and the transparent section is decreased. For example, when semiconductor integrated circuits are manufactured by using the photomasks of the sixteenth embodiment, it is possible to increase the performance of the image formation. In addition, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of each photomask is more flat when comparing with each photomask shown in FIG.21A, FIG.21E, FIG.21G, FIG.21H, FIG.22A, FIG.22E, FIG.22G, and FIG.22H.

#### Seventeenth embodiment.

FIG.23A to FIG.23H are diagrams showing photomasks, each has a phase shift pattern of an intermediate phase type. In particular, FIG.23B to FIG.23D, FIG.23F show the photomasks, each has the shift pattern of the intermediate phase type that can be formed only by the fabrication methods of the second to tenth embodiments according to the present invention. Sections other than the boundary section 70 in the photomasks having the phase shift pattern of the intermediate phase type shown in FIG.23A, FIG.23E, FIG.23G, and FIG.23H are formed by using the conventional photomask fabrication methods. Other components are the same as those of the first to tenth embodiments, therefore the explanation of them is omitted here.

Next, a description will be given of the operation of the photomasks having the phase shift pattern of the intermediate



phase type as the seventeenth embodiment.

When comparing with the photomasks having the phase shift pattern of the intermediate phase type shown in FIG.23A, FIG.23E, FIG.23G, and FIG.23H, the surface of each of the photomasks having the phase shift pattern of the edge highlighting type shown in FIG.23B to FIG.23D, and FIG.23F is flat, and the shifter adjacent to the shade pattern in each of the photomasks has a unique thickness. Thereby, the function of the phase control can be increased and the contrast of the optical image can also be increased. In addition to this features, no mechanical stress happens by the brush 41 (see FIG.7A) during the washing process. This means each of the photomasks of the seventeenth embodiment has a high durability.

As described above, according to the seventeenth embodiment, because the topography of the surface of each photomask is flat, the phase control can be easily performed, so that the contrast of an optical image can be increased. That is, as shown in FIG.23B to FIG.23D, and FIG.23F, because the difference of the step at the shifter section is smaller in height, the effect of a wave guide is relaxed, and the difference between the shifter section and the transparent section is decreased. For example, when semiconductor integrated circuits are manufactured by using the photomasks of the seventeenth embodiment, it is possible to increase the performance of the image formation. In addition, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush 41 (see FIG.7A) during the washing process because the topography of the surface of each photomask is more flat when comparing with each of the photomasks shown in FIG.23A, FIG.23E, FIG.23G, and FIG.23H.

Eighteenth embodiment.

FIG.24 is a diagram showing a projection exposure apparatus (or a step and repeat exposure apparatus) to be used for fabricating semiconductor integrated circuits such as a liquid crystal display device by using the photomasks of the present invention. In FIG.24, the reference number 215 designates one of the photomasks of the present invention, 216 designates a wafer to be used during the semiconductor manufacturing process for manufacturing semiconductor integrated circuits, and 217 denotes a resist film coated on the wafer 216. The projection exposure apparatus shown in FIG.24 is used for the manufacturing process of the semiconductor integrated circuits.

Next, a description will be given of the operation of the projection exposure apparatus shown in FIG.24.

When semiconductor integrated circuits such as liquid crystal display devices are manufactured by using the photomask 215 of the present invention, the photomask 215 is placed in the projection exposure apparatus shown in FIG.24. Then, the resist film 217 is coated on the wafer 216.

Next, a light source generates and radiates a radiation ray such as a monochromatic ray, an electron beam, a laser beam, or a X ray on the photomask 215 through lenses. Thereby, the pattern of the photomask 215 is printed on the resist 217 coated on the wafer 216. After this exposure process of the wafer 216, the developing process, the etching process, and the elimination process of the photo resist are performed in order. It is possible to repeat these processes if required. Finally, the desired semiconductor integrated circuits are fabricated.

As described above, according to the eighteenth embodiment, the photomask 215 having a high quality that is free from defects of the shade pattern, as one of the photomasks obtained by the first to seventeenth embodiments, is placed in the projection exposure apparatus shown in FIG.24 and then exposed under the radiation ray such as a monochromatic ray, an electron beam, a laser beam, or a X ray generated by the light source in order to fabricate the semiconductor integrated circuit such as the liquid crystal display and other devices. Therefore it is possible to increase the phase control of the photomask and also to increase the contrast of the optical image on the resist film 217 on the wafer 216. This features causes to increase the yield of the semiconductor integrated circuits with a high quality.

As described above in detail, according to the present invention, each of the photomasks of the present invention has the configuration in which the shade pattern is formed in the hollow section in the transparent substrate such as the transparent glass substrate, and the surface of the shade pattern and the surface of the transparent glass substrate are formed on a same plane. Therefore the topography of the photomask is flat. That is, a complicated surface is formed on the photomask, not like the conventional photomask. This configuration of the photomask of the present invention has a high durability to the mechanical stress caused in the chemical and mechanical polishing during the washing process. This causes to fabricate the photomask with a high quality that is free from the defect of the shade pattern. To use the photomask of the present invention for manufacturing semiconductor integrated circuits such as liquid crystal displays and so on

can increases the yield of the semiconductor integrated circuits.

In addition, in the photomasks according to the present invention, many kinds of the phase shift patterns in the photomasks in which the phase shift pattern has a unique thickness and the surface of the phase shift pattern is flat are formed. It is therefore possible to fabricate the photomask in which no shade section is formed in the boundary section between the phase shift pattern and the transparent glass section and no phase defect is formed at the boundary section. In addition, the difference of the step at the shifter section is smaller in height, the effect of a wave guide is reduced, and the difference between the shifter section and the transparent section is decreased. For example, when semiconductor integrated circuits are manufactured by using the photomasks of the present invention, it is possible to increase the performance of the image formation. In addition, it is possible to form the photomask having a high durability to the mechanical stress caused by the brush during the washing process because the topography of the surface of the photomask is flat when comparing with the conventional one. Therefore it is possible to increase the yield of semiconductor integrated circuits manufactured by using the photomask of the present invention because the function of the phase control can be increased and the contrast of the optical image can also be increased.

While the above provides a full and complete disclosure of the preferred embodiments of the present invention, various modifications, alternate constructions and equivalents may be employed without departing from the scope of the invention.

Therefore the above description and illustration should not be construed as limiting the scope of the invention, which is defined by the appended claims.

RECEIVED

## WHAT IS CLAIMED IS:

1. A photomask comprising:

a transparent substrate;

5 a hollow section formed on a surface of said transparent substrate;

a shade pattern including a shade section, said shade section made up of a shade film and formed in said hollow section; and

10 reflection preventing sections, each formed according to one of cases:

each reflection preventing section formed on said shade section;

15 each reflection preventing section formed under said shade section; and

each reflection preventing section formed on and under said shade section.

2. A photomask comprising:

20 a transparent substrate;

a shade pattern formed on said surface of said transparent substrate; and

a phase shift pattern selectively formed on said shade pattern and said transparent substrate,

25 wherein a surface of said phase shift pattern is flat.

3. A photomask according to claim 2, wherein an end section of said phase shift pattern that is contacted to said transparent substrate has a sloped shape that is gradually thin.

4. A photomask according to claim 2, wherein a difference of a step between said phase shift pattern and said transparent substrate at said end section of said phase shift pattern that is contacted to said transparent substrate is gradually small.

5

5. A photomask comprising:  
a transparent substrate;  
a hollow section formed on a surface of said transparent substrate;

10 a shade pattern made up of a shade film, said shade film formed in said hollow section; and

a phase shift pattern, whose surface is flat, selectively formed on said transparent substrate having said shade pattern formed in said hollow section.

15

6. A photomask according to claim 5, wherein a thickness of an end section of said phase shift pattern contacted to said transparent substrate is gradually thin.

20

7. A photomask comprising:  
a transparent substrate;  
a hollow section formed on a surface of said transparent substrate;

25 a shade pattern made up of a shade film, said shade film formed in said hollow section; and

a phase shift pattern formed on said transparent substrate including said shade pattern formed in said hollow section.

30

8. A photomask according to claim 7, wherein an end section

of said phase shift pattern that is contacted to said transparent substrate has a sloped shape that is gradually thin.

9. A photomask according to claim 2, wherein said phase shift  
5 pattern includes a phase shift pattern of a Levenson's type.

10. A photomask according to claim 2, wherein said phase shift  
pattern includes a phase shift pattern of an auxiliary shifter  
type.

10

11. A photomask according to claim 2, wherein said phase shift  
pattern includes a phase shift pattern of an edge highlighting  
type.

12. A photomask according to claim 2, wherein said phase shift  
15 pattern includes a phase shift pattern of a half tone type.

13. A photomask according to claim 2, wherein said phase shift  
pattern includes a phase shift pattern of a half tone type with  
20 a shade pattern.

25

14. A photomask according to claim 2, wherein said phase shift  
pattern includes a phase shift pattern of a shifter shading type  
with a shade pattern.

15. A photomask according to claim 2, wherein said phase shift  
pattern includes a phase shift pattern of an intermediate phase  
type.

30 16. A photomask fabrication method comprising the steps of:



forming a resist film on a transparent substrate;

forming a desired pattern on said resist film by developing said resist film after said resist film is selectively exposing by using a radiation ray;

5        forming hollow sections in said transparent substrate by selectively etching said transparent substrate by using said resist film as a mask;

eliminating said resist film from said transparent substrate;

10       forming a first reflection preventing film in each of said hollow sections;

forming a shade film on said first reflection preventing film formed in each of said hollow sections;

15       forming a shade pattern by performing a chemical and mechanical polishing for said shade film; and

forming a second reflection preventing film on said shade pattern;

20       wherein said processes for forming said first reflection preventing process and said second reflection preventing process are performed selectively.

17. A photomask fabrication method comprising the steps of:

forming a shade pattern on a transparent substrate;

25       coating a phase shift film on both said transparent substrate and said shade pattern;

forming a phase shift pattern by selectively etching said phase shift film by using a radiation ray; and

30       performing a chemical and mechanical polishing for a surface of said phase shift pattern in order to form said phase shift pattern having a desired thickness.

18. A photomask fabrication method according to claim 17, after the step of forming said phase shift pattern, further comprises the steps of:

5       selectively etching said phase shift pattern by using said radiation ray; and

          performing said chemical and mechanical polishing for said surface of said phase shift pattern that has been etched in order to form said phase shift pattern having a desired  
10       thickness and a flat surface.

19. A photomask fabrication method according to claim 17, after the step of forming said shade pattern on said transparent substrate, further comprises the steps of:

15       forming a resist film on said shade pattern;

          forming a resist pattern by selectively etching said resist film by using said radiation ray;

          selectively etching said transparent substrate by using said resist pattern as a mask;

20       eliminating said resist pattern from said transparent substrate; and

          performing said chemical and mechanical polishing for said surface of said phase shift pattern.

25       20. A photomask fabrication method comprising the steps of:

          forming a resist film on a transparent substrate;

          forming a desired pattern on said resist film by developing said resist film after said resist film is selectively exposed by using a radiation ray;

30       forming hollow sections in said transparent substrate by

selectively etching said transparent substrate by using said resist film as a mask;

eliminating said resist film from said transparent substrate;

5        forming a shade film in each of said hollow sections;  
performing a chemical and mechanical polishing for said shade film in order to form a shade pattern;  
forming a phase shift film on said transparent substrate;  
and

10       forming a phase shift pattern by selectively etching said phase shift film by using said radiation ray.

21.    A photomask fabrication method according to claim 20,  
after the step of forming said phase shift pattern by  
15       selectively etching said phase shift film by using said radiation ray, further comprises the step of:

performing said chemical and mechanical polishing for said phase shift pattern formed on said transparent substrate.

20    22.   A photomask fabrication method comprising the steps of:  
forming a resist film on a transparent substrate;  
forming a desired pattern on said resist film by  
developing said resist film after said resist film is  
selectively exposed by using a radiation ray;

25       forming hollow sections in said transparent substrate by selectively etching said transparent substrate by using said resist film as a mask;

eliminating said resist film from said transparent substrate;

30       forming a shade film in each of said hollow sections;

performing a chemical and mechanical polishing for said shade film in order to form a shade pattern;

forming a resist film on said transparent substrate in which said shade pattern has been formed;

5 selectively etching said resist film by using said radiation ray; and

selectively etching said transparent substrate.

23. A photomask fabrication method according to claim 22,  
10 after the step of selectively etching said transparent substrate by using said radiation ray, further comprises the step of:

performing said chemical and mechanical polishing for said transparent substrate.

15

24. A photomask fabrication method according to claim 16, wherein one of an electron beam, a laser beam, and a monochromatic beam is used as said radiation ray.

20

25. A fabrication method of semiconductor integrated circuits such as a liquid crystal display, wherein said photomask according to claim 1 is used.

25

26. A fabrication method of semiconductor integrated circuits such as a liquid crystal display, wherein said photomask fabrication method according to claim 16 is included.

## ABSTRACT OF THE DISCLOSURE

A photomask fabricated by a photomask fabrication method has a transparent substrate (10), a shade pattern (11) formed in a hollow section (23), and a phase shift pattern (102) having  
5 a flat surface that is selectively formed on the transparent substrate (10) and the shield pattern (11).

10

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30

FIG.1A (PRIOR ART)

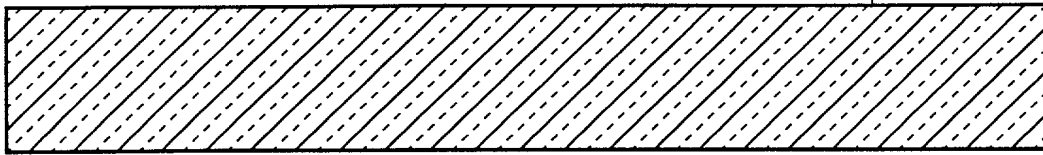


FIG.1B (PRIOR ART)

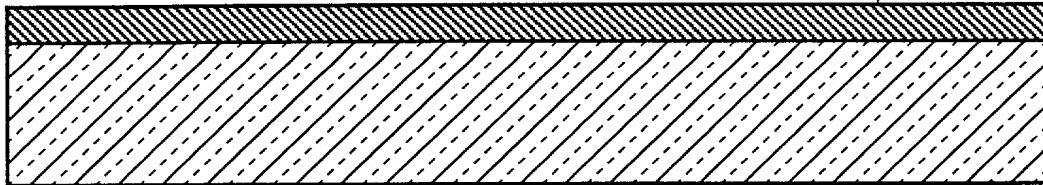


FIG.1C (PRIOR ART)

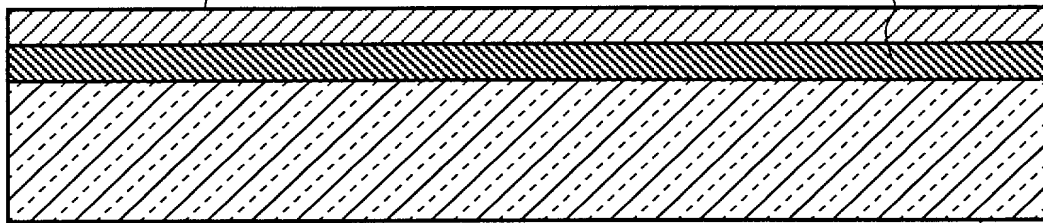


FIG.1D (PRIOR ART)

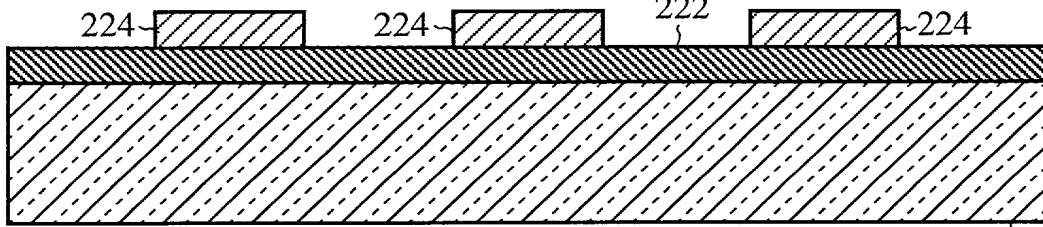


FIG.1E (PRIOR ART)

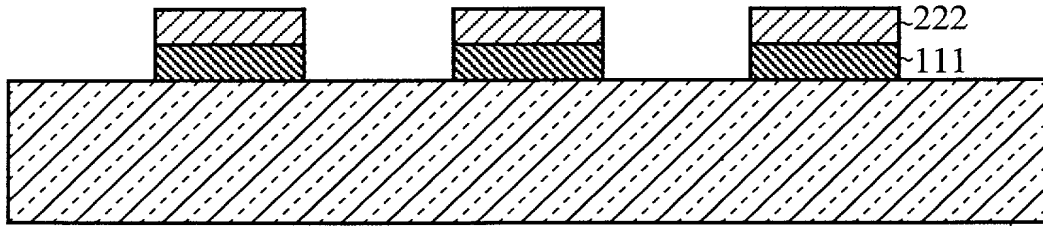


FIG.1F (PRIOR ART)

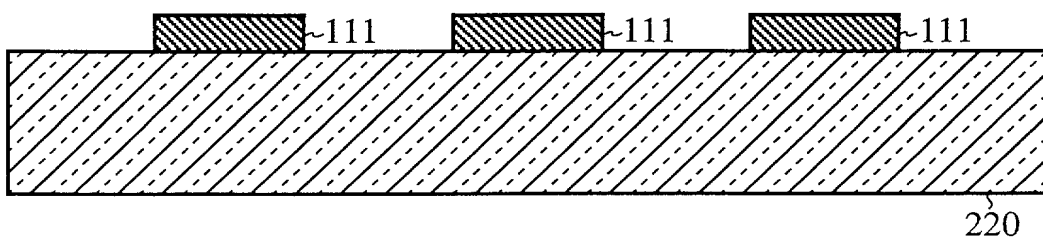


FIG.2A (PRIOR ART)

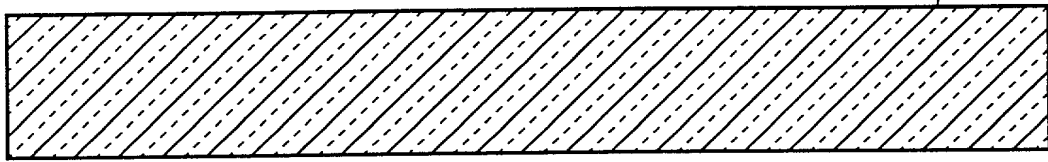


FIG.2B (PRIOR ART)

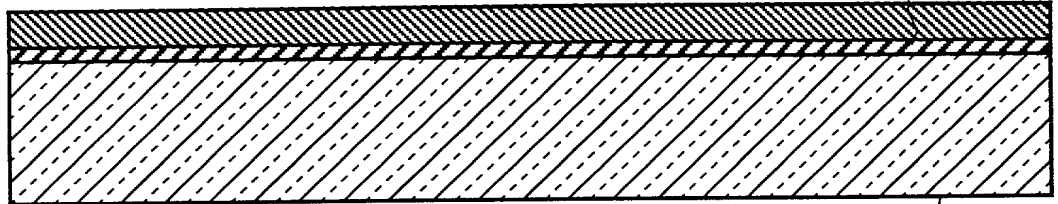


FIG.2C (PRIOR ART)

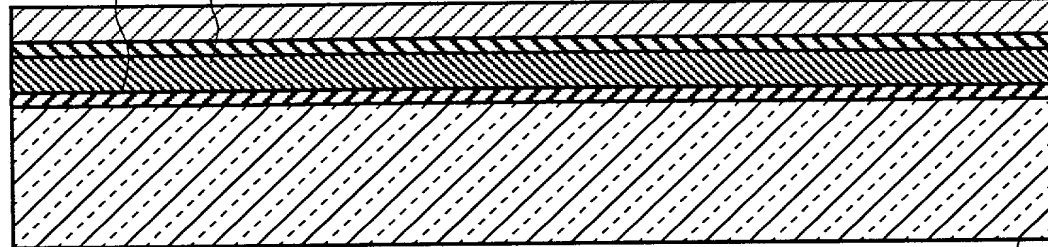


FIG.2D (PRIOR ART)

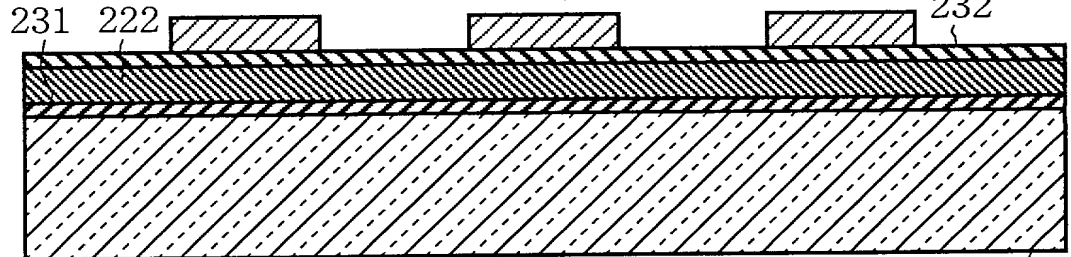


FIG.2E (PRIOR ART)

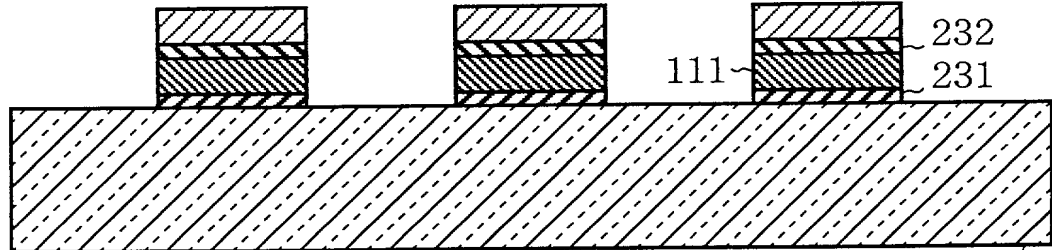


FIG.2F (PRIOR ART)

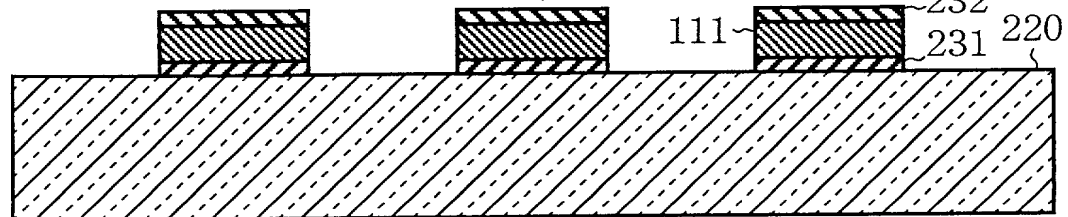


FIG.3A (PRIOR ART)

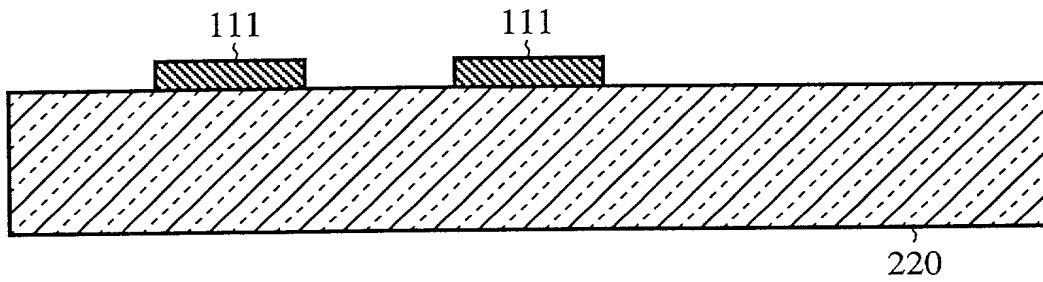


FIG.3B (PRIOR ART)

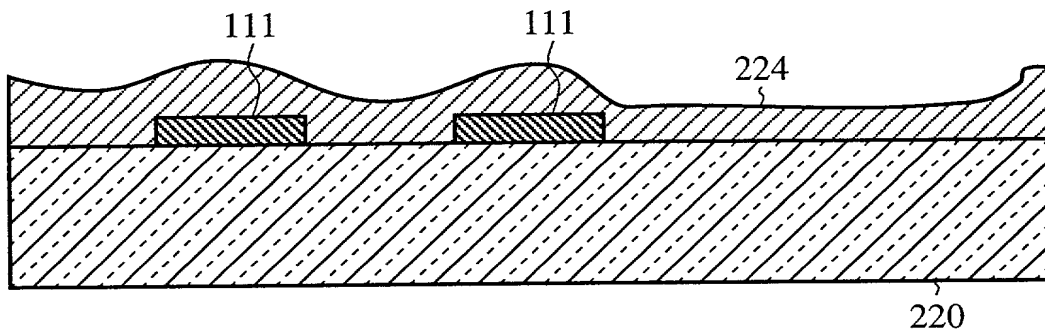


FIG.3C (PRIOR ART)

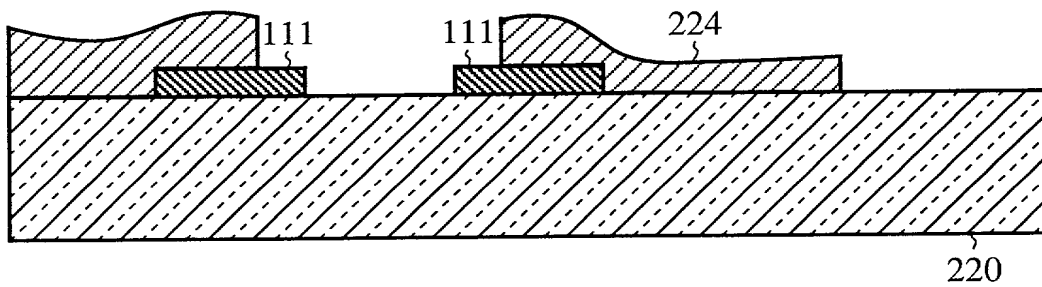


FIG.3D (PRIOR ART)

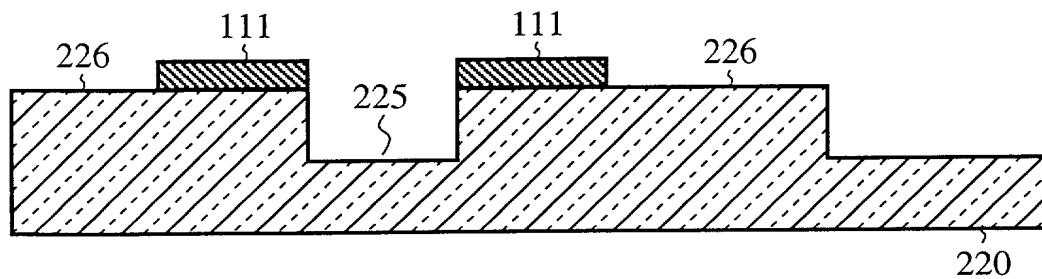




FIG.4

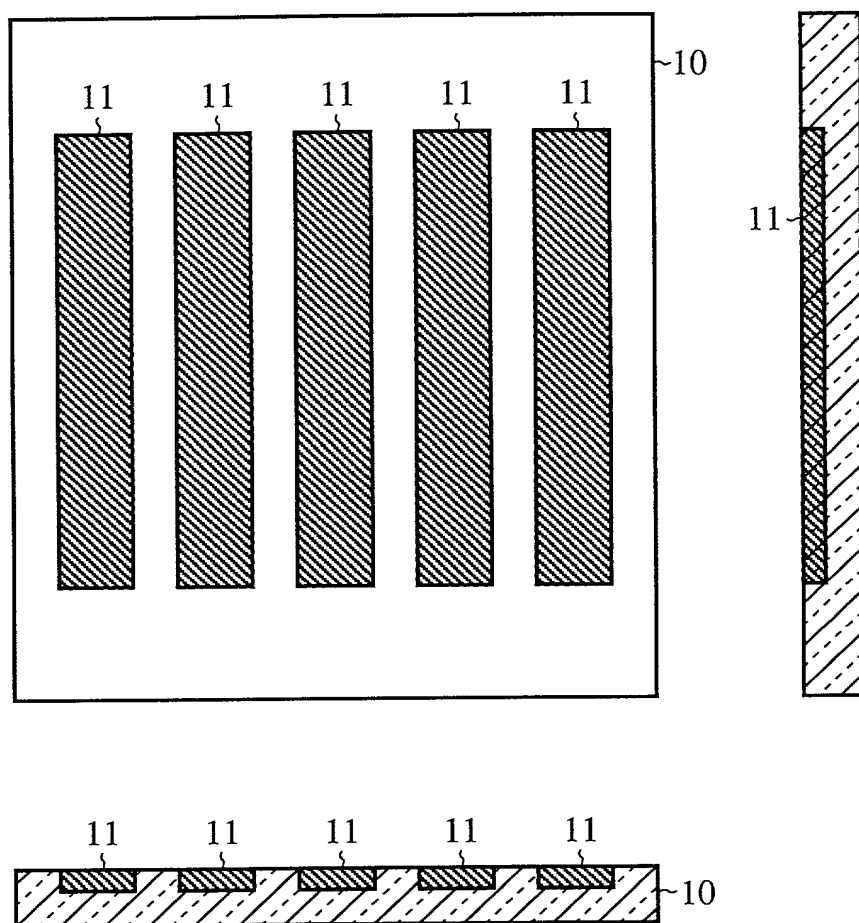


FIG.5A

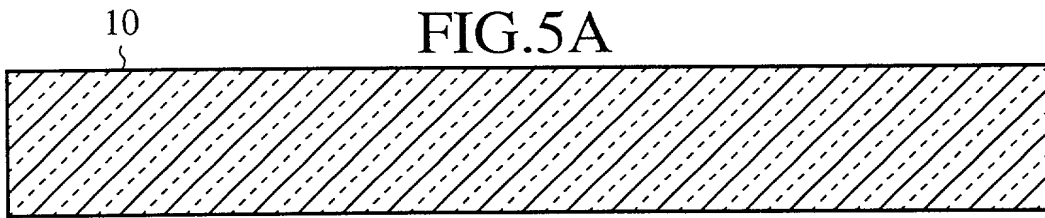


FIG.5B

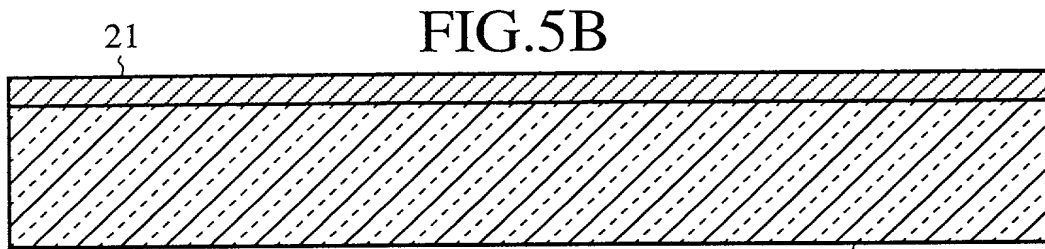


FIG.5C

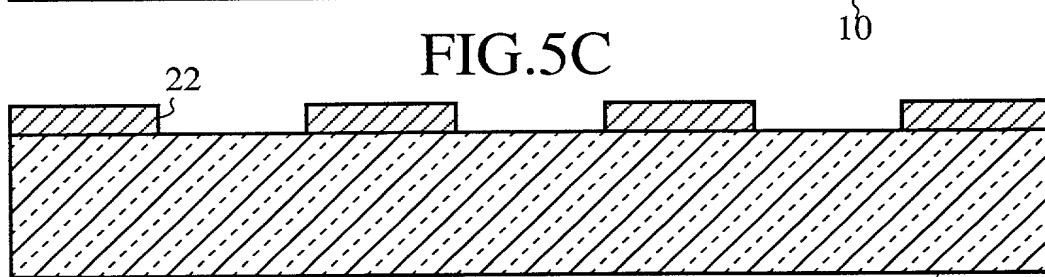


FIG.5D

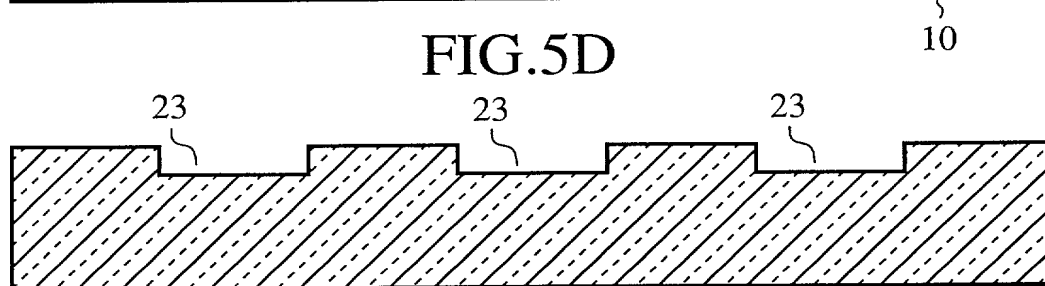


FIG.5E

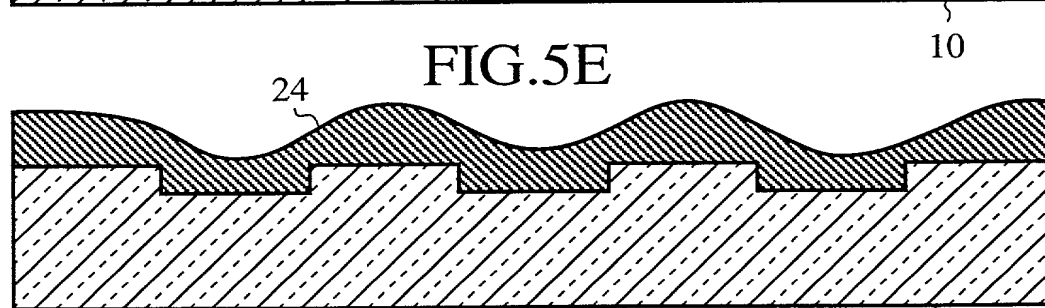


FIG.5F

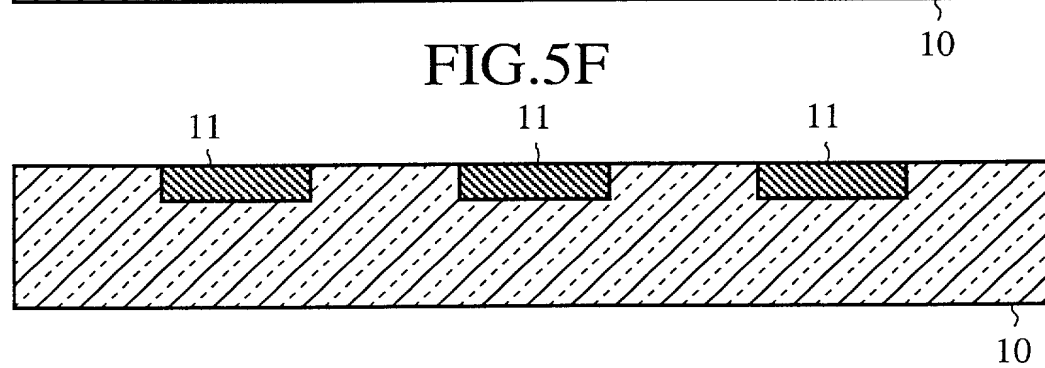


FIG.6

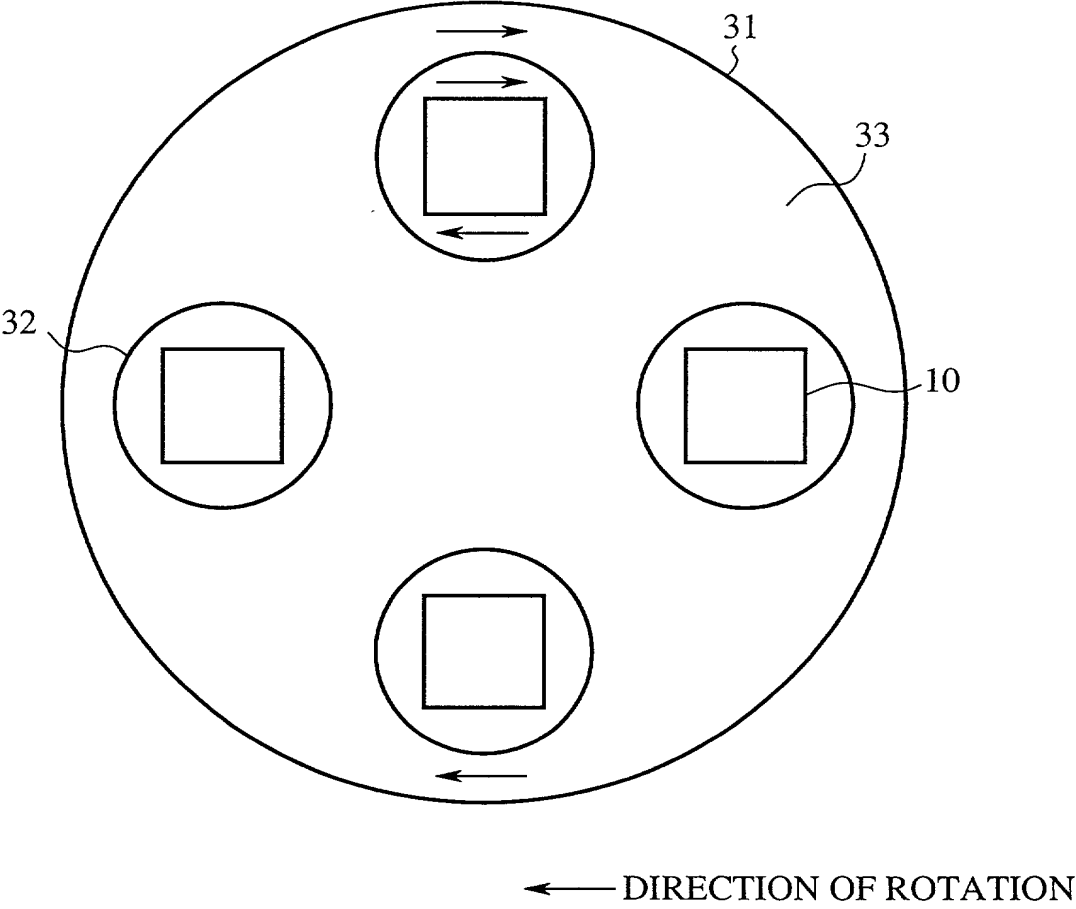


FIG. 7A

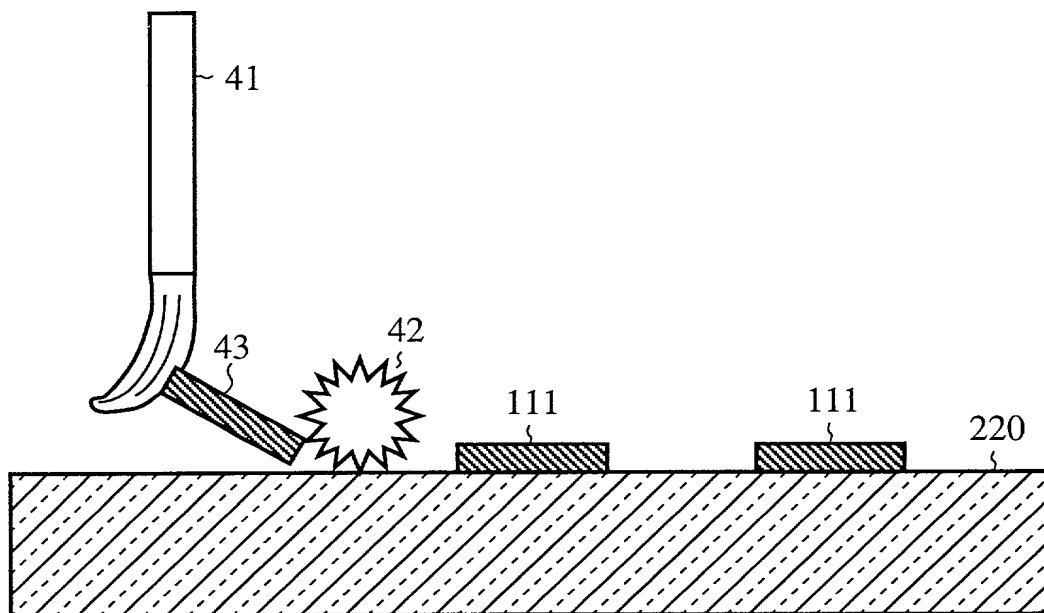


FIG. 7B

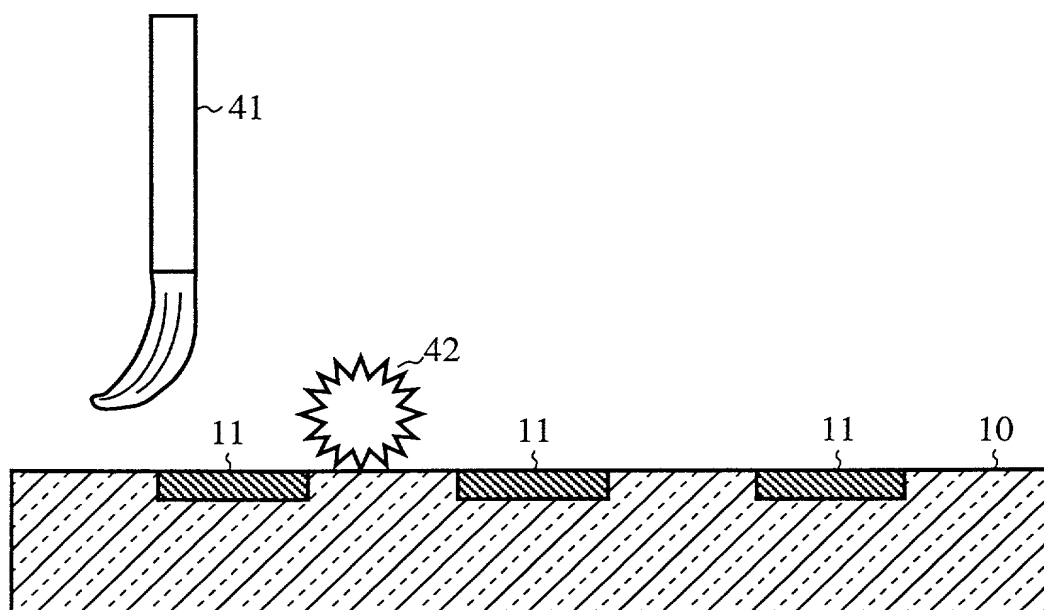


FIG. 8A

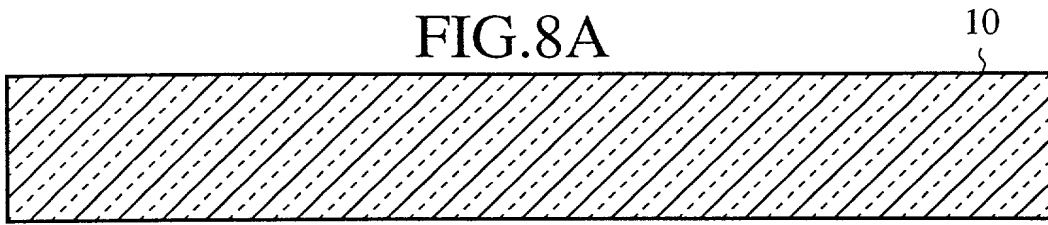


FIG. 8B

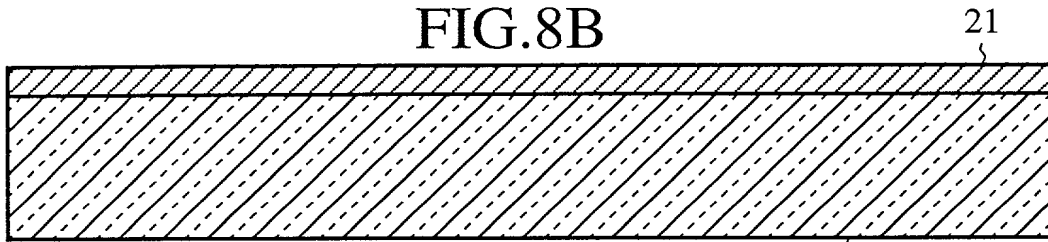


FIG. 8C

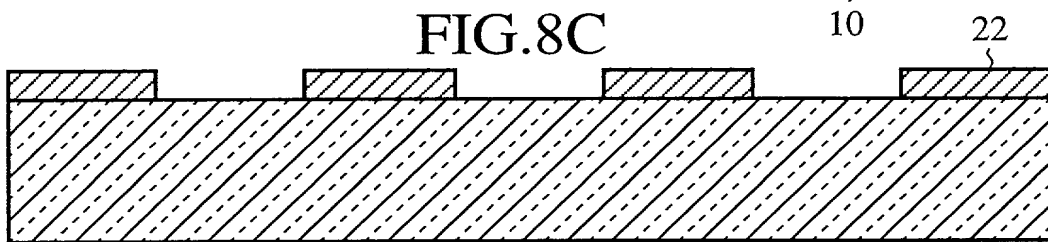


FIG. 8D

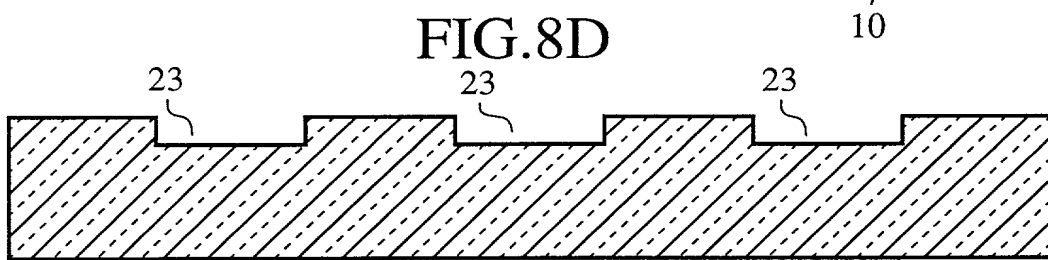


FIG. 8E

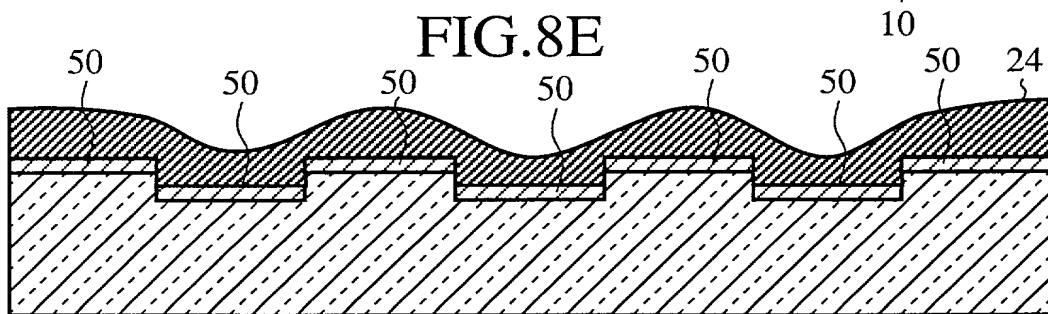


FIG. 8F

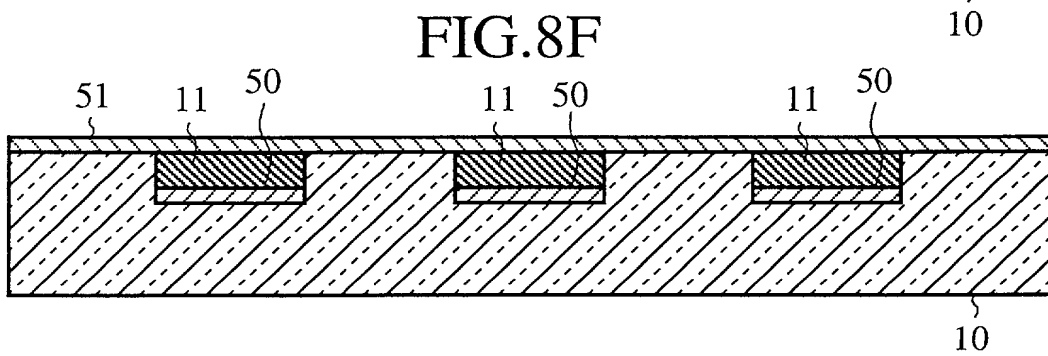


FIG.9A

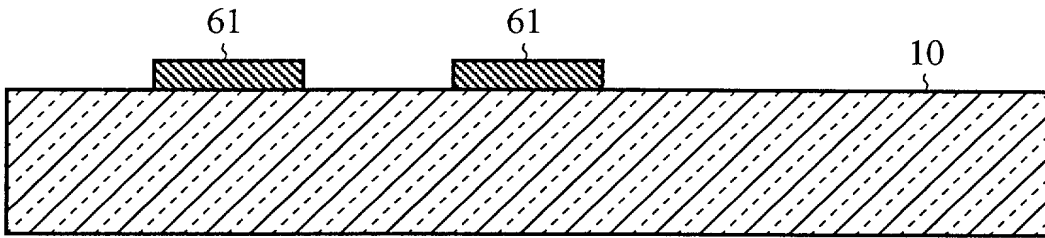


FIG.9B

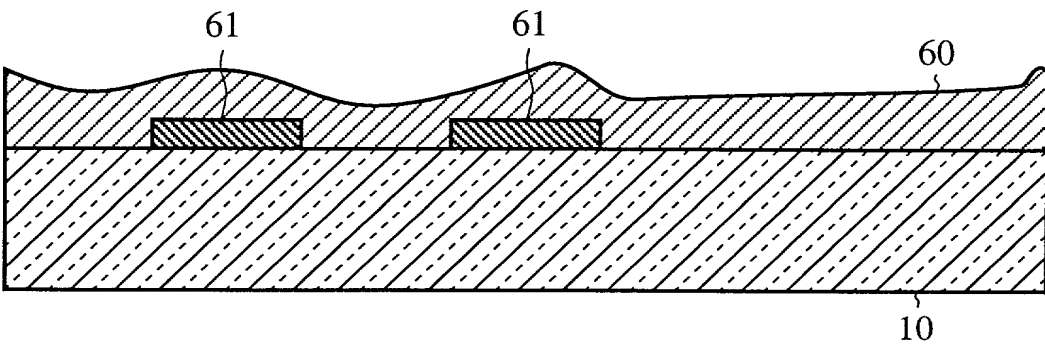


FIG.9C

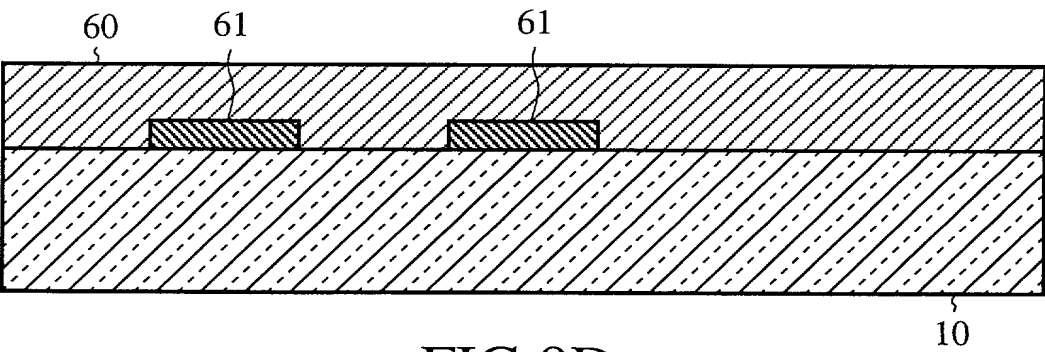


FIG.9D

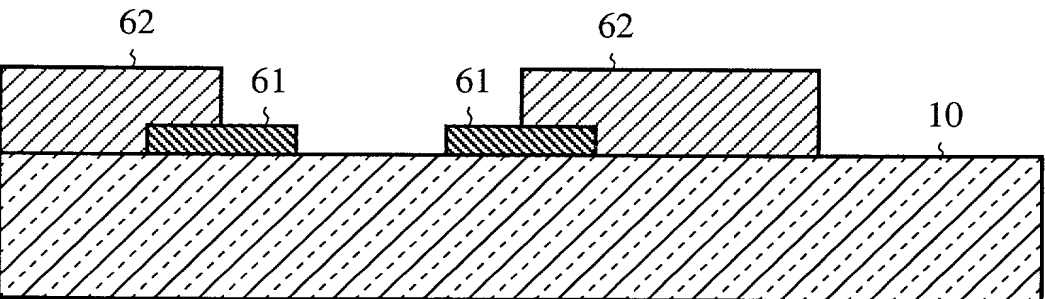


FIG.10A

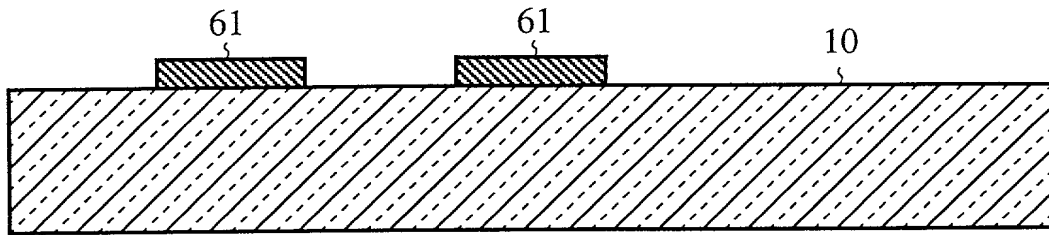


FIG.10B

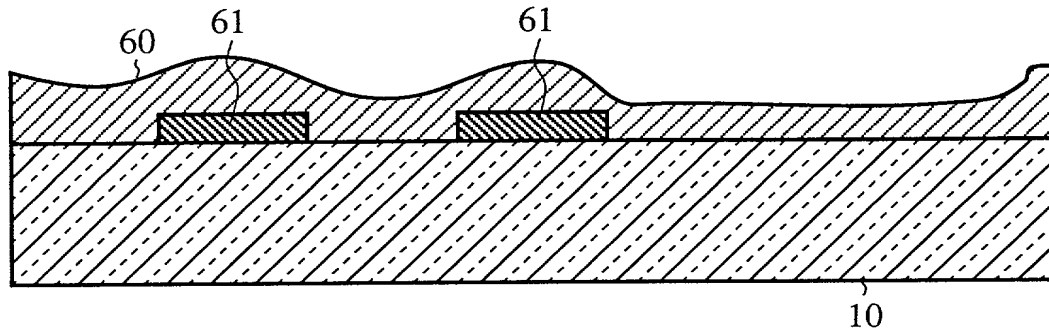


FIG.10C

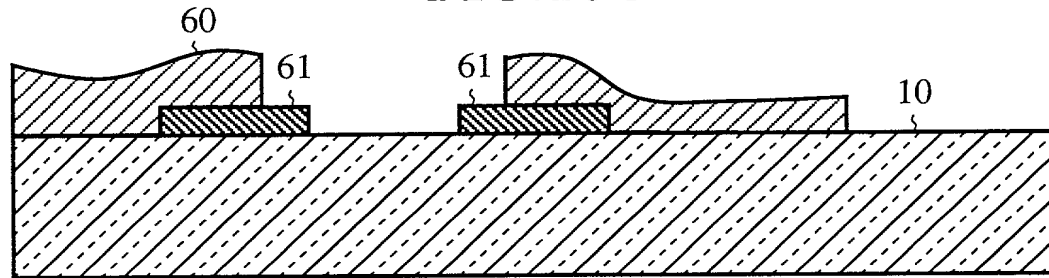


FIG.10D

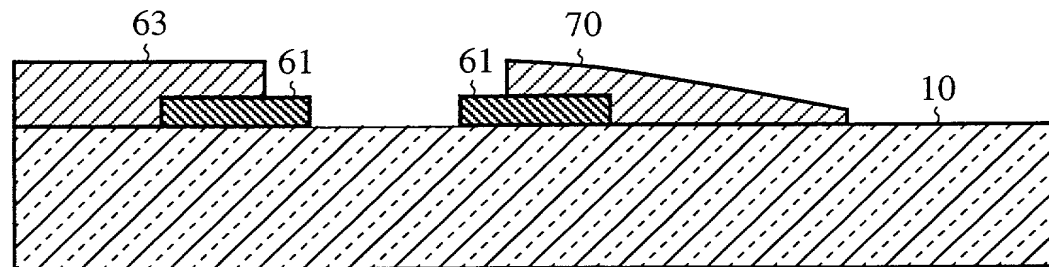


FIG.11A

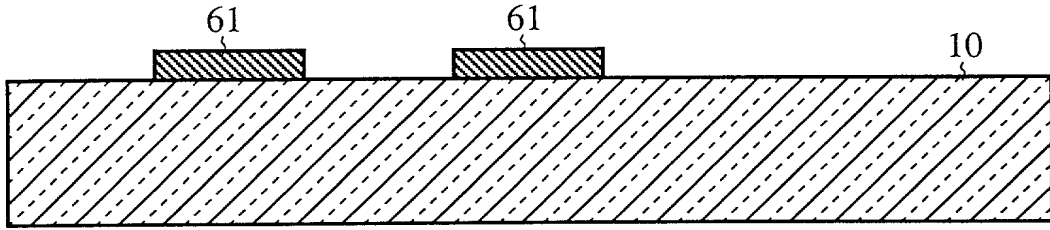


FIG.11B

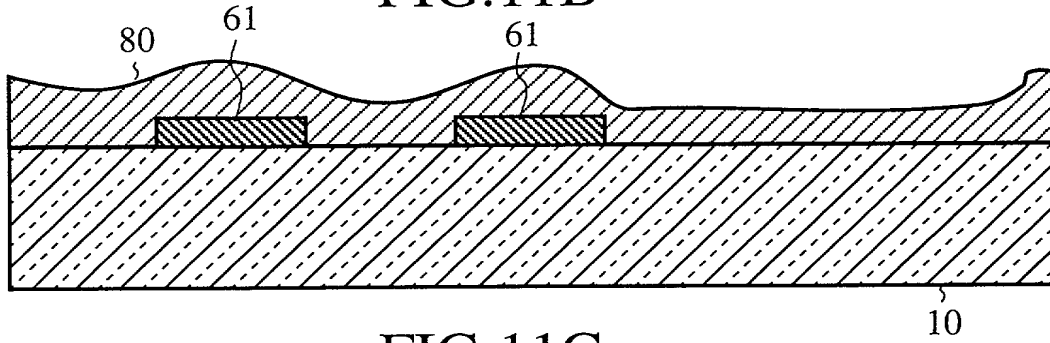


FIG.11C

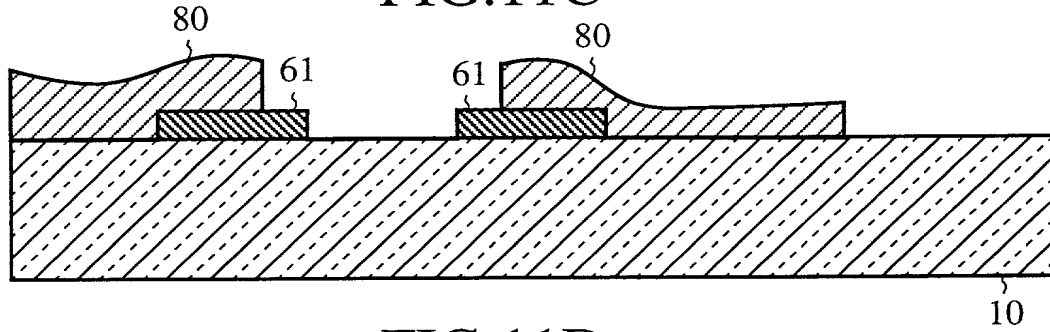


FIG.11D

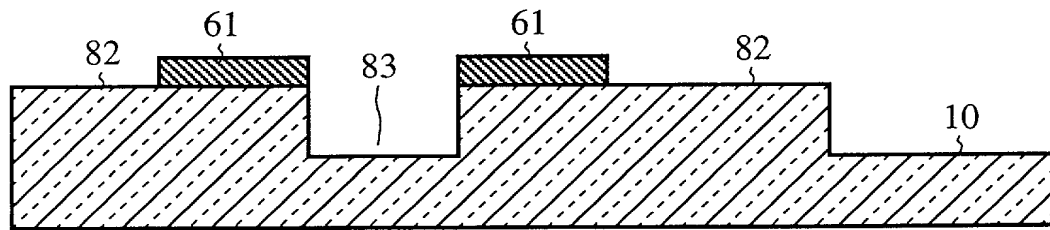


FIG.11E

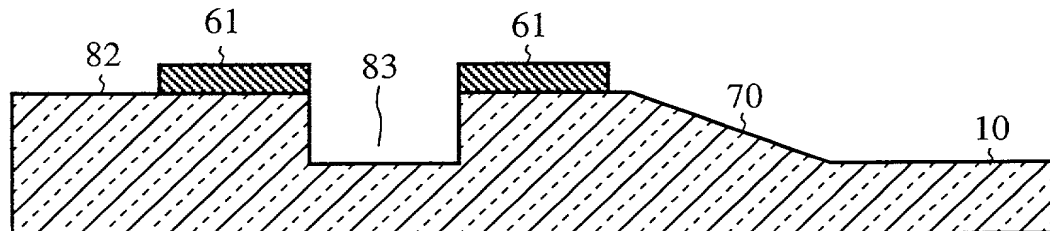




FIG.12A

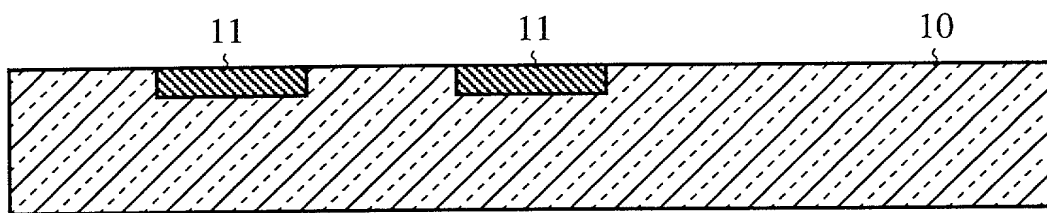


FIG.12B

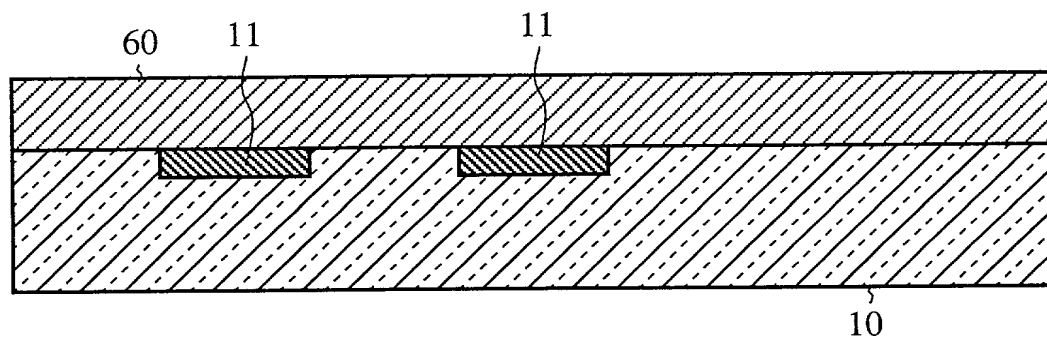


FIG.12C

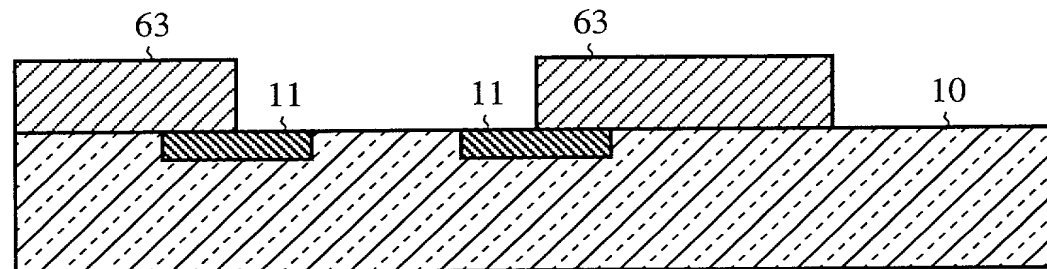


FIG.12D

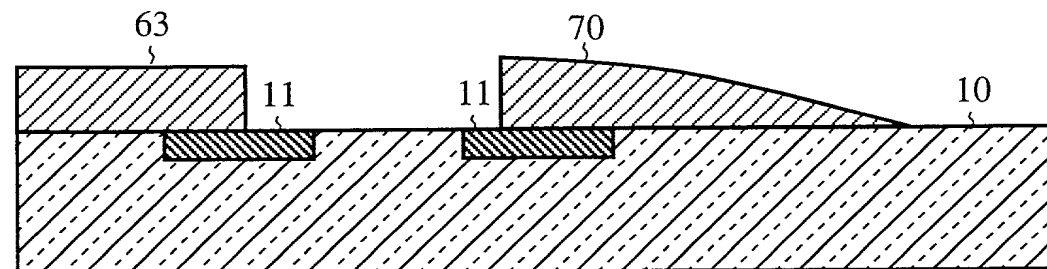


FIG.13A

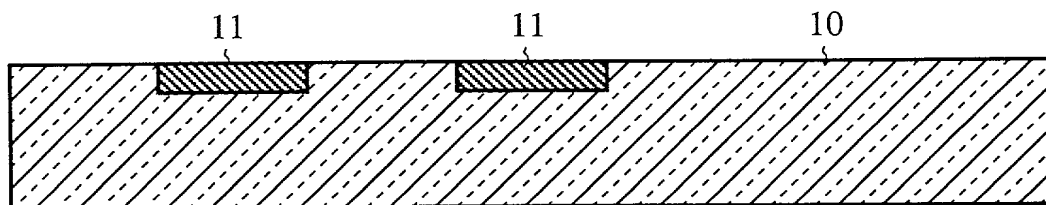


FIG.13B

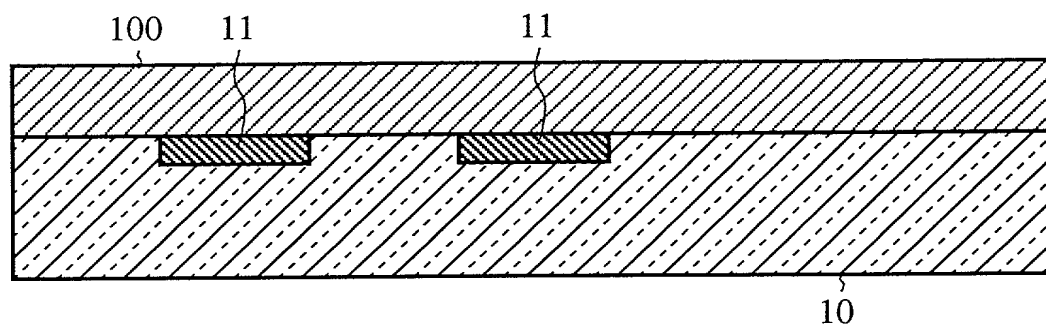


FIG.13C

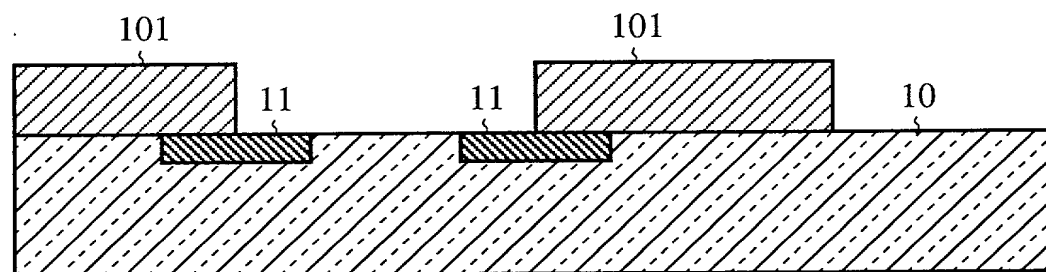


FIG.13D

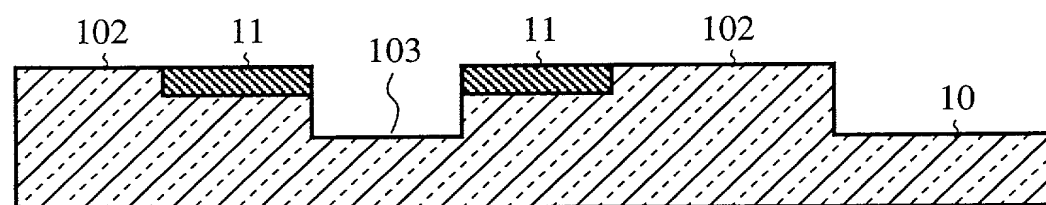


FIG.13E

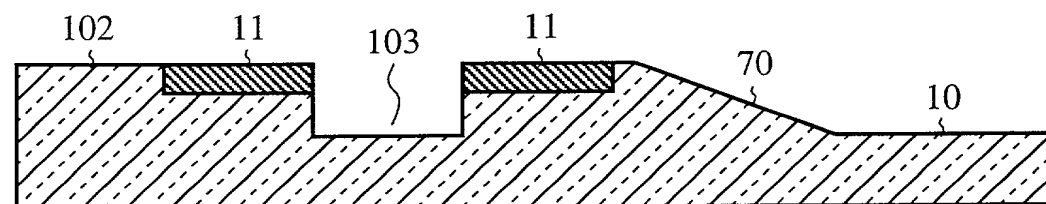


FIG.14A

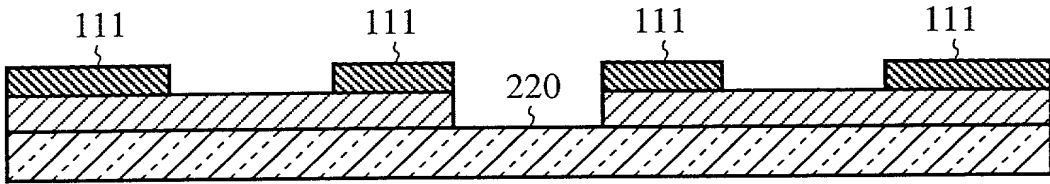


FIG.14B

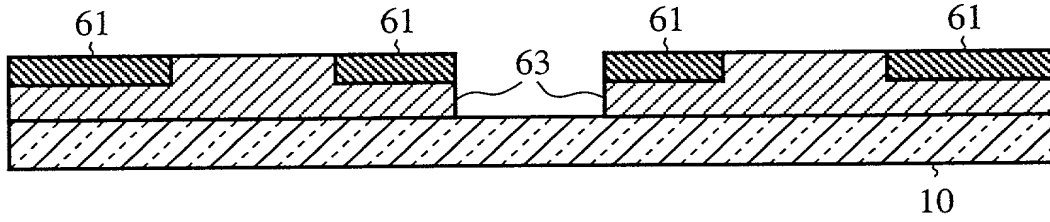


FIG.14C

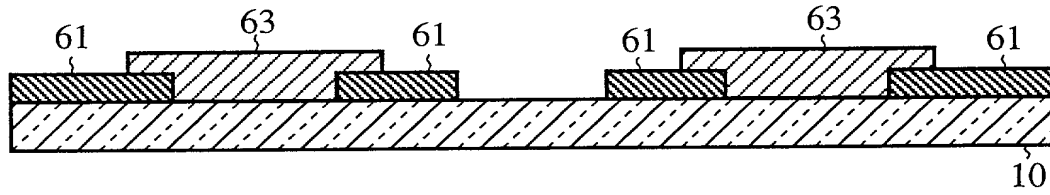


FIG.14D

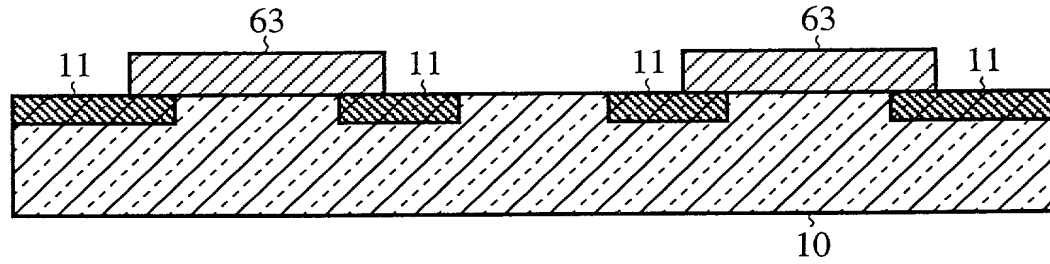


FIG.14E

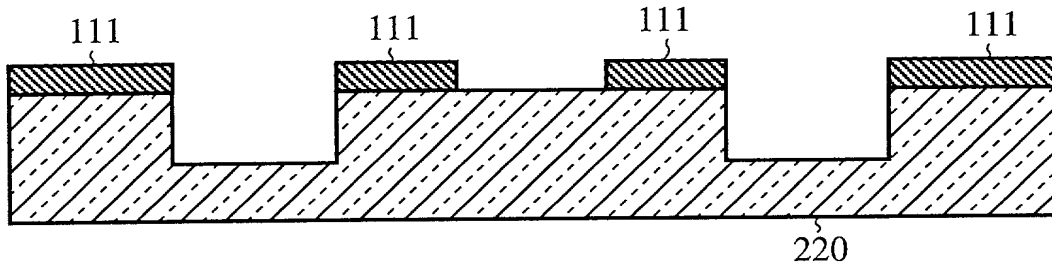


FIG.14F

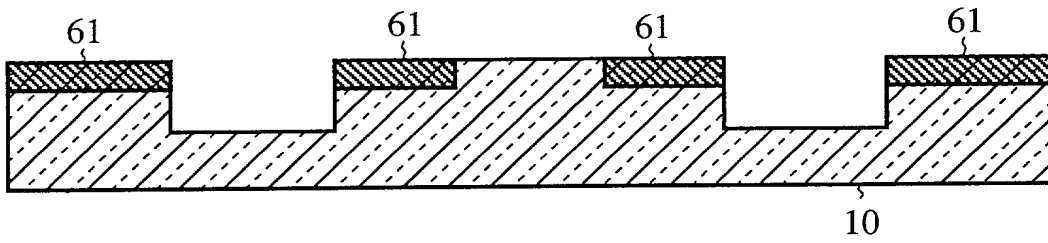


FIG.14G

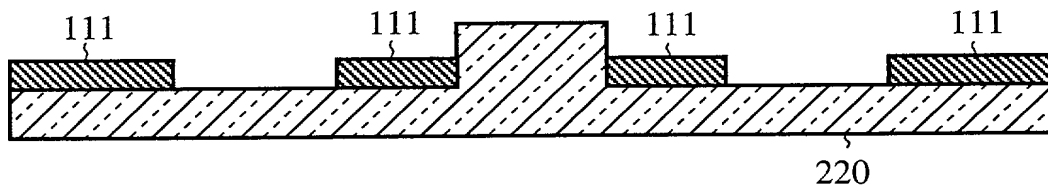


FIG.14H

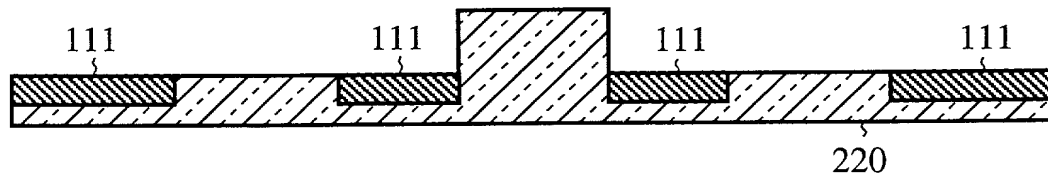


FIG.15A

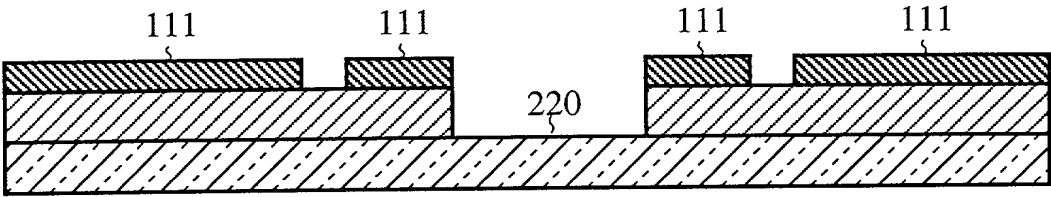


FIG.15B

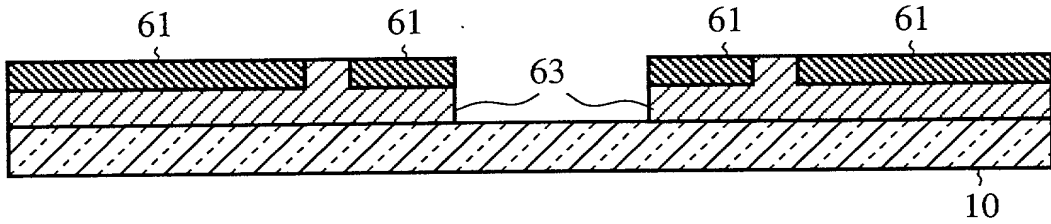


FIG.15C

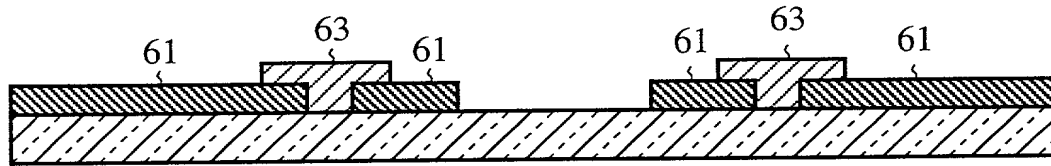


FIG.15D

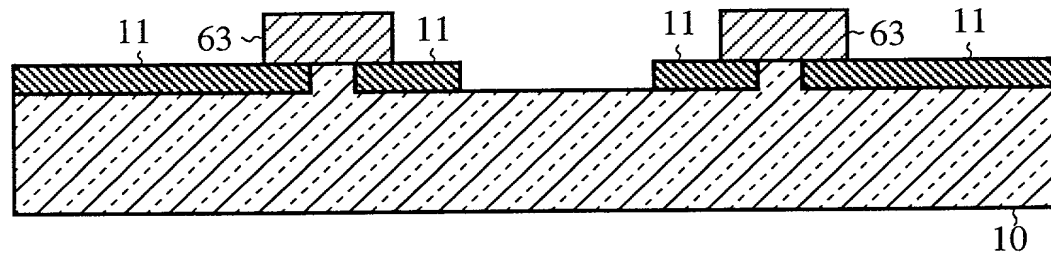


FIG.15E

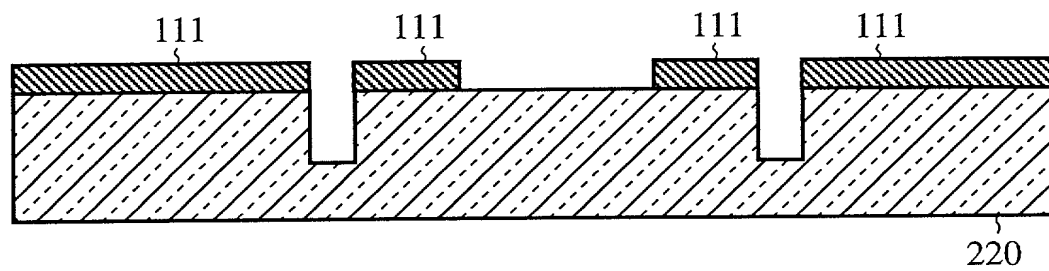


FIG.15F

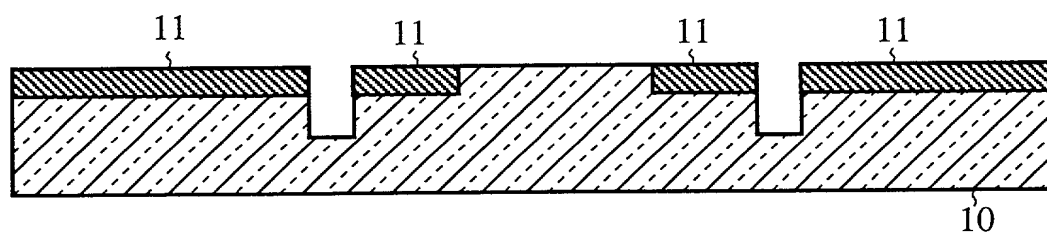


FIG.15G

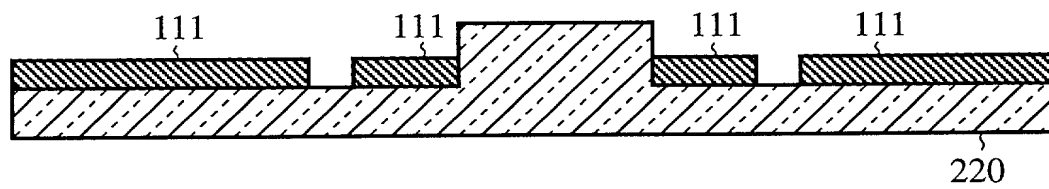


FIG.15H

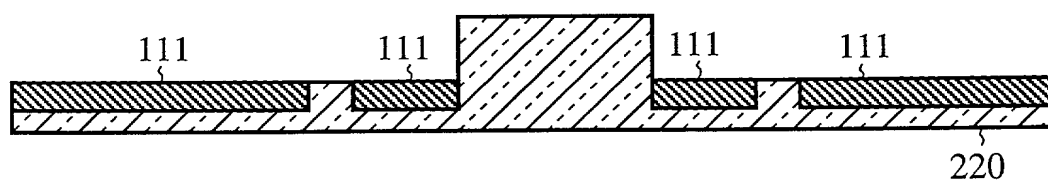


FIG.16A

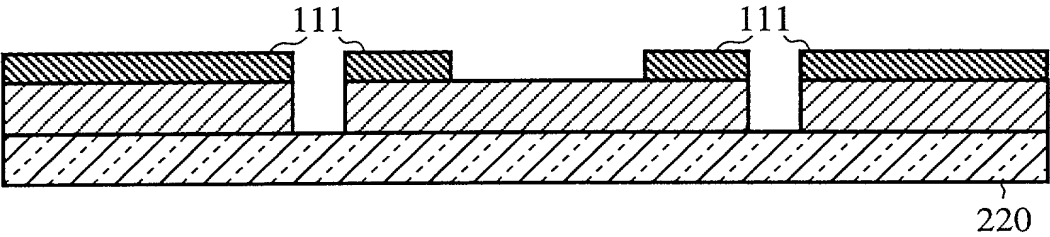


FIG.16B

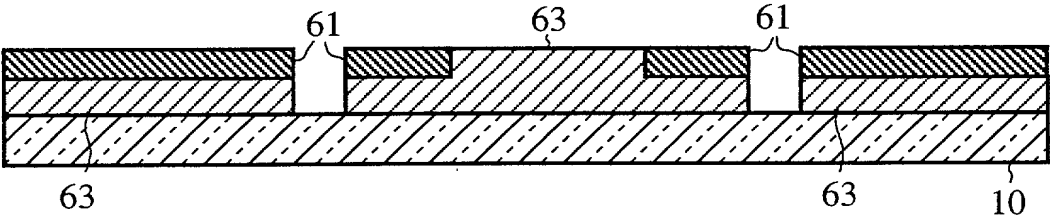


FIG.16C

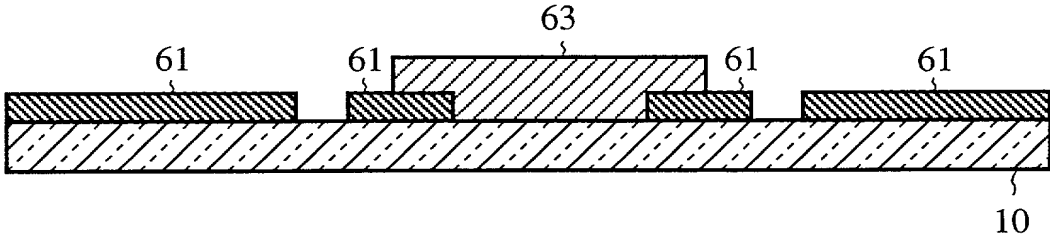


FIG.16D

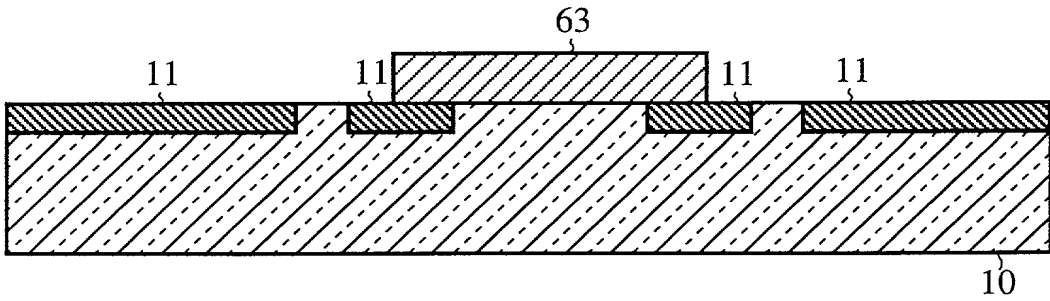


FIG.16E

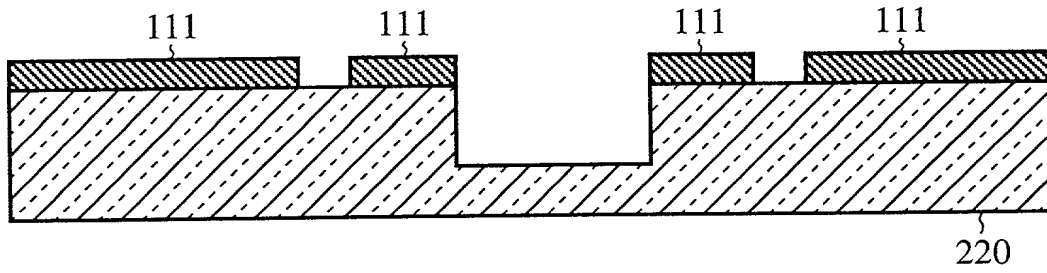


FIG.16F

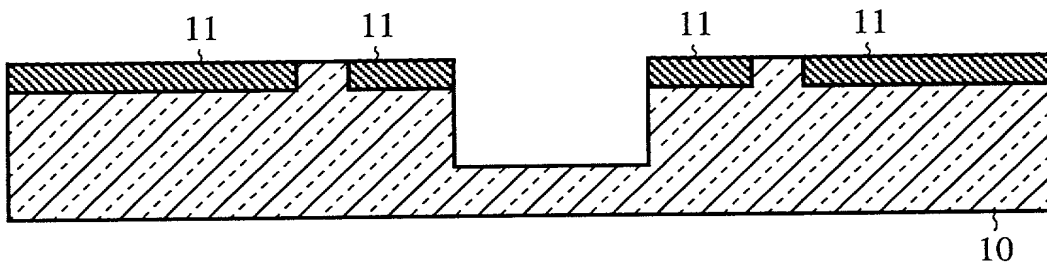


FIG.16G

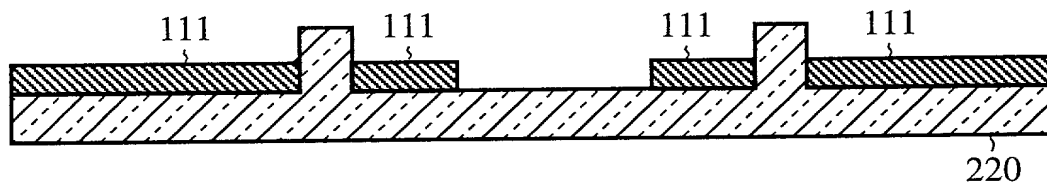


FIG.16H

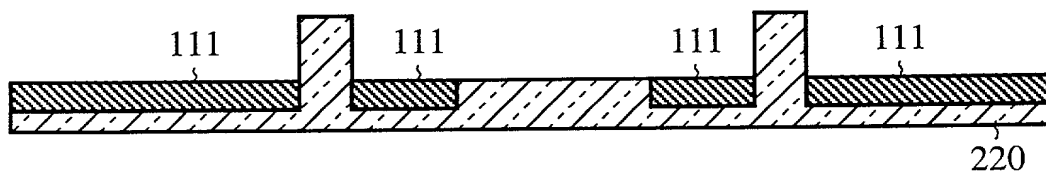




FIG.17A

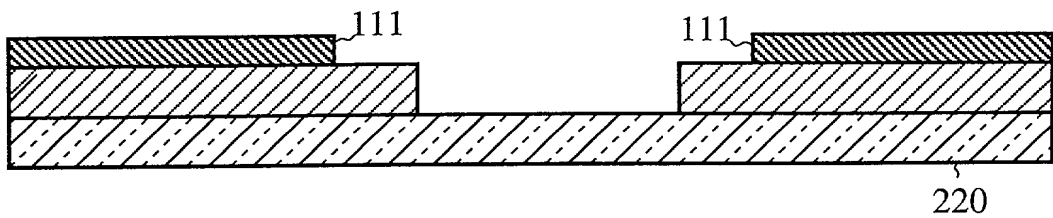


FIG.17B

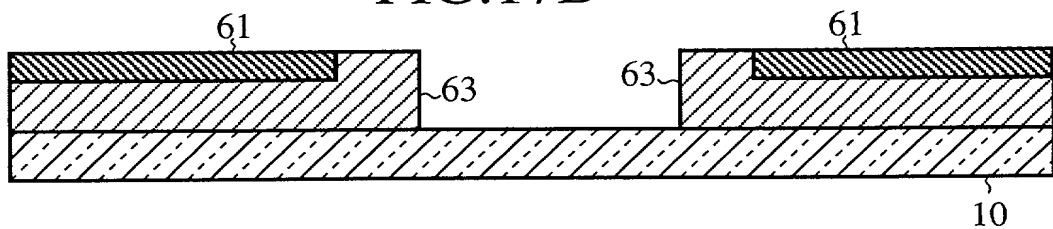


FIG.17C

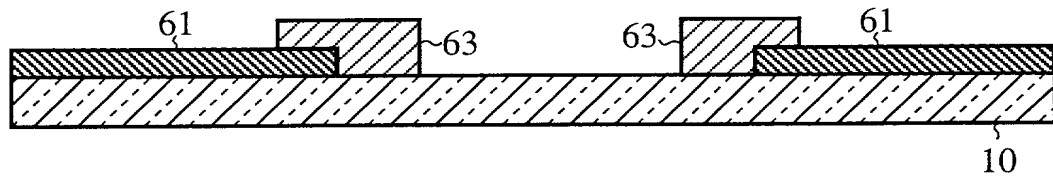


FIG.17D

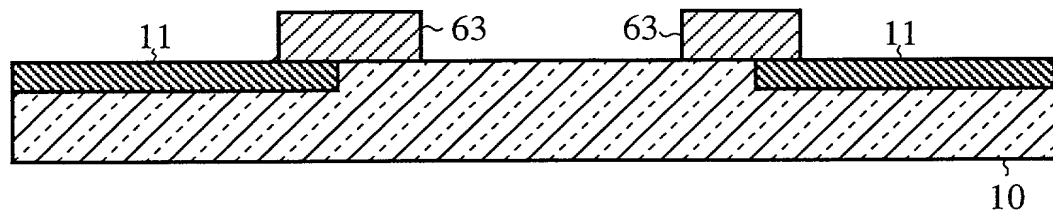


FIG.17E

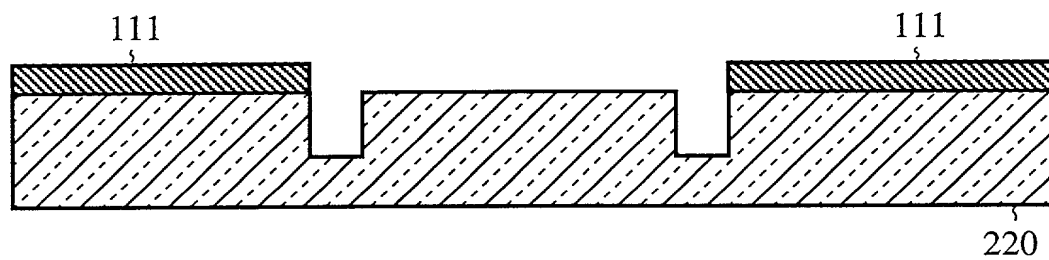


FIG.17F

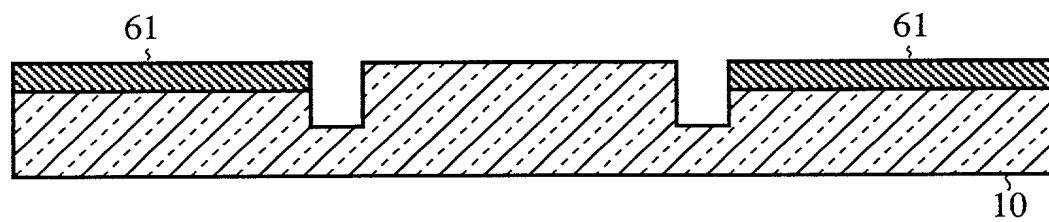


FIG.17G

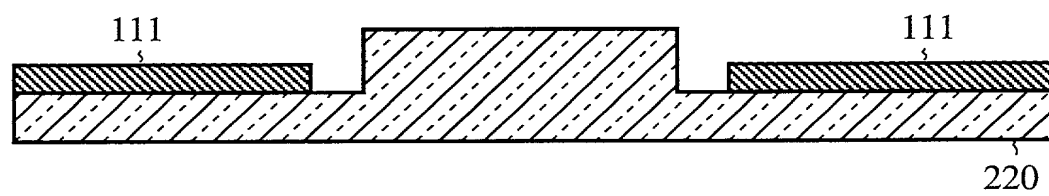


FIG.17H

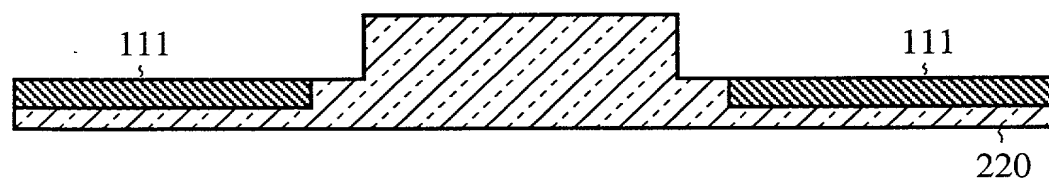


FIG.18A

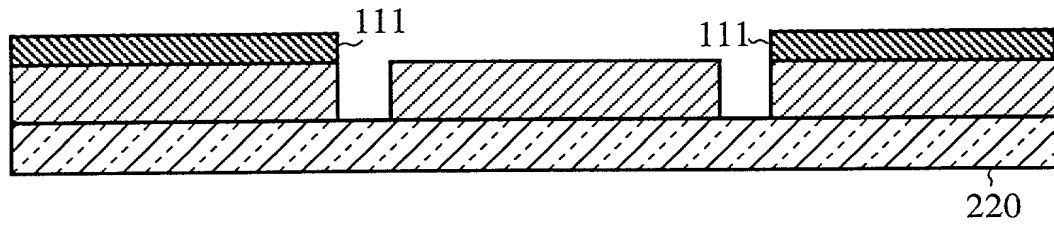


FIG.18B

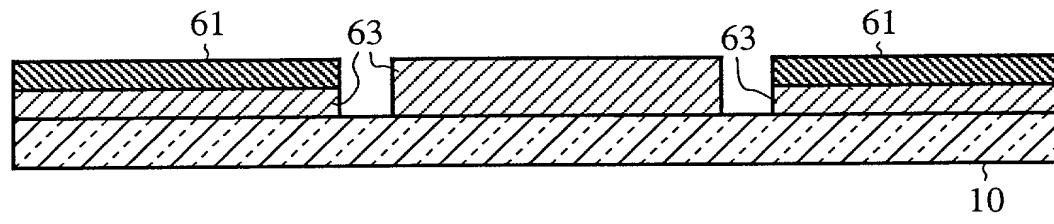


FIG.18C

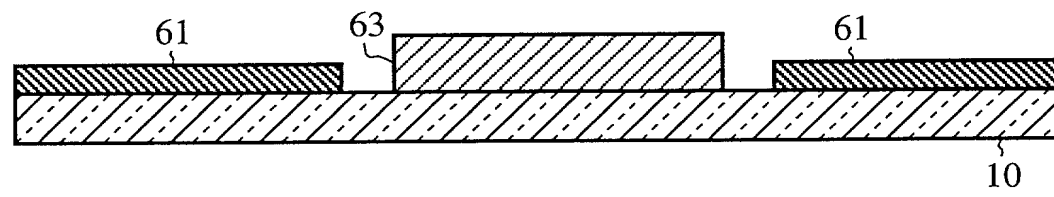


FIG.18D

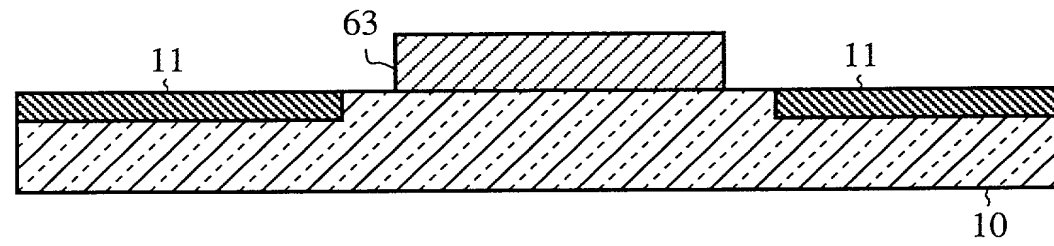


FIG.18E

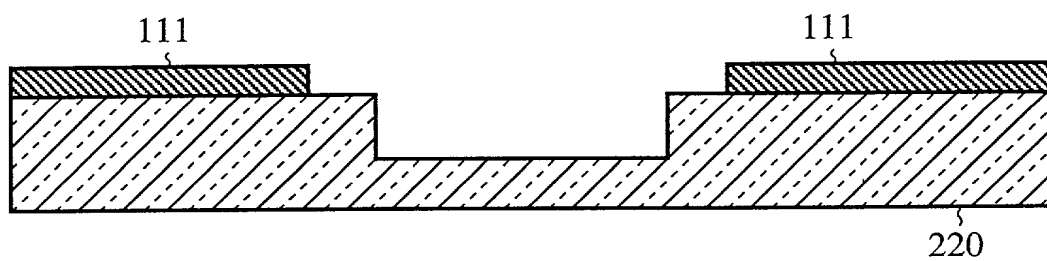


FIG.18F

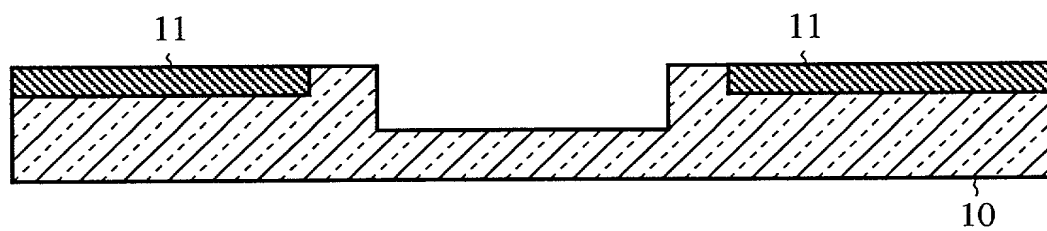


FIG.18G

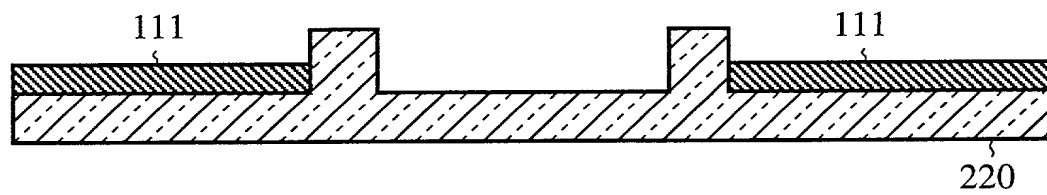


FIG.18H

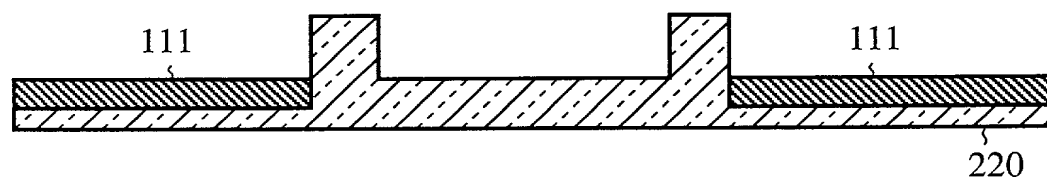


FIG.19A

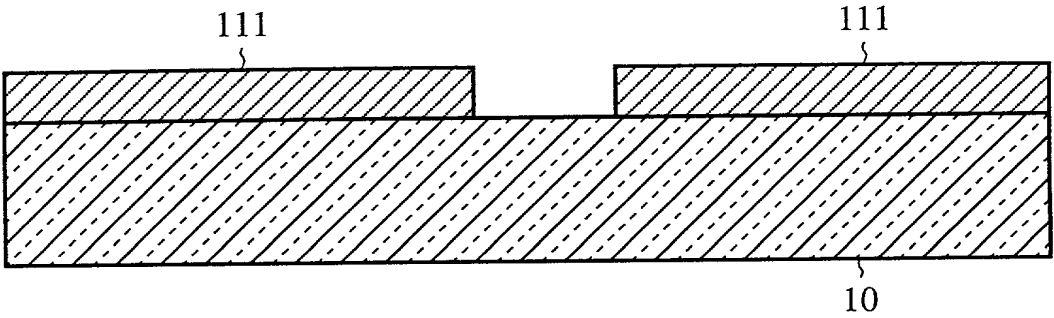


FIG.19B

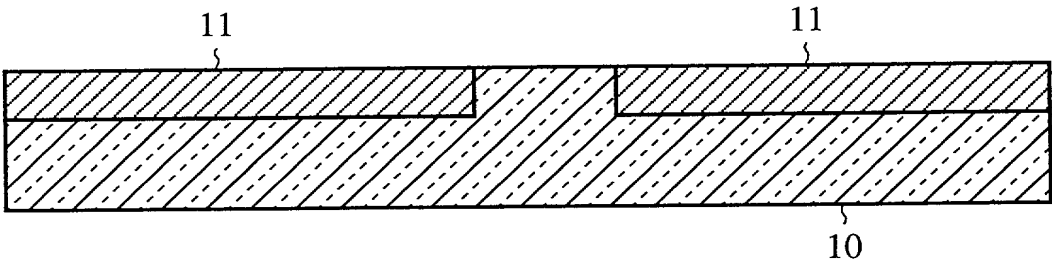


FIG.20A

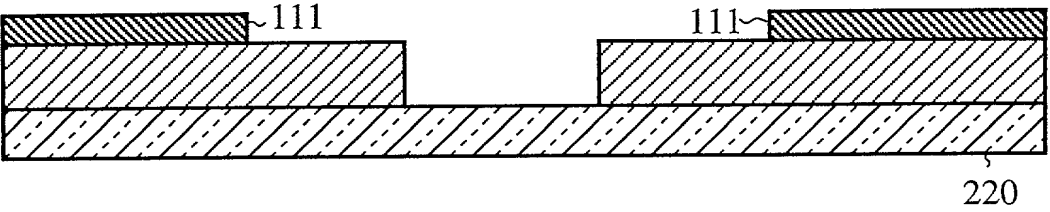


FIG.20B

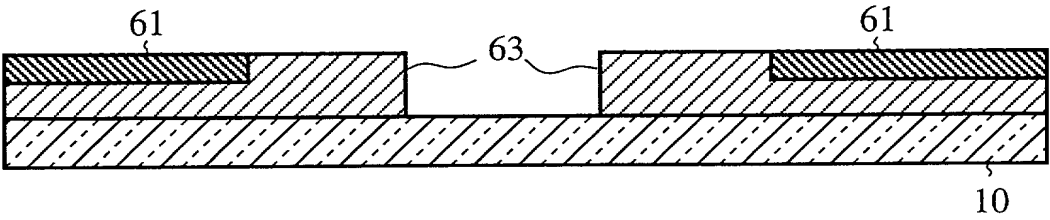


FIG.20C

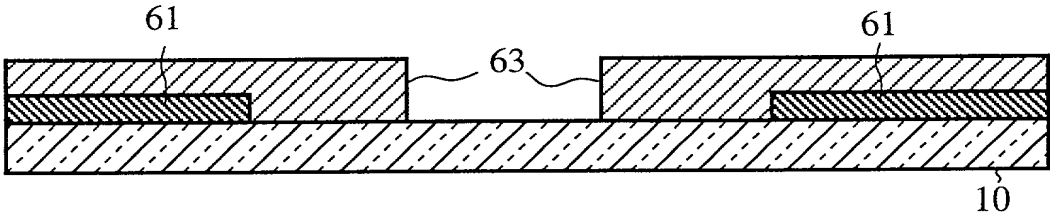


FIG.20D

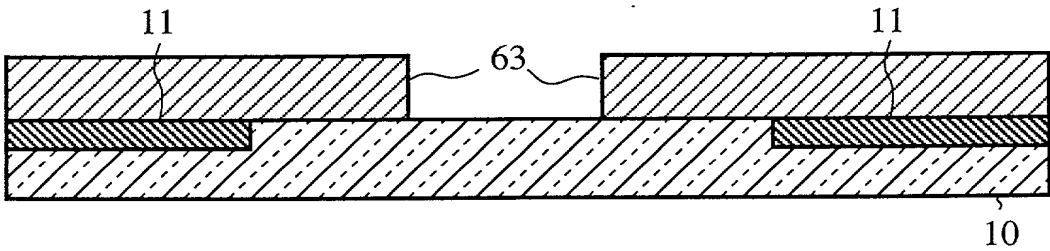


FIG.20E

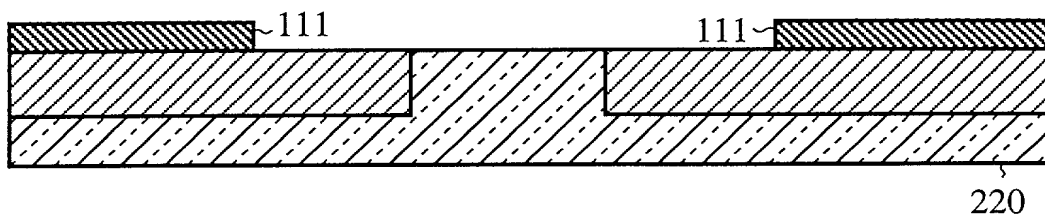


FIG.20F

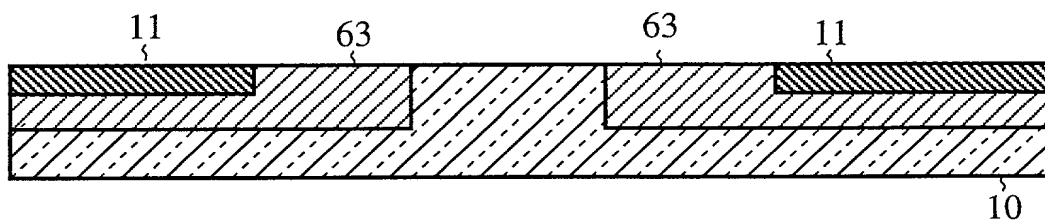


FIG.20G

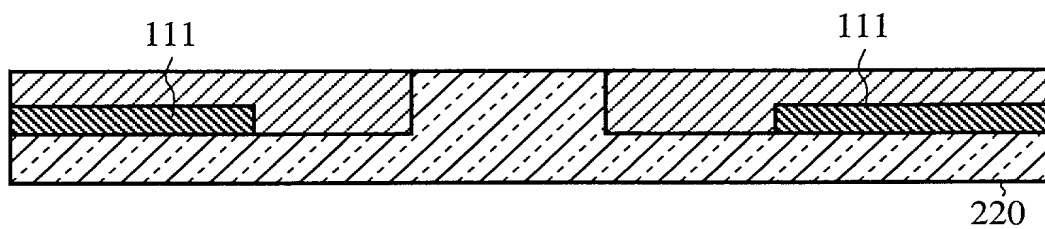


FIG.20H

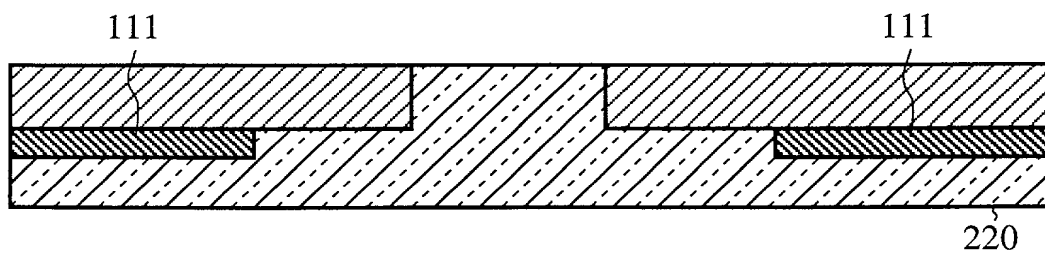


FIG.21A

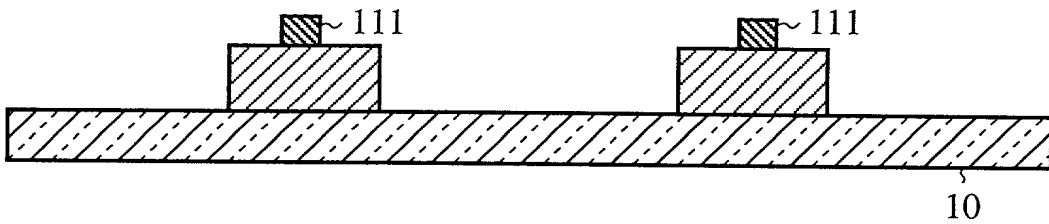


FIG.21B

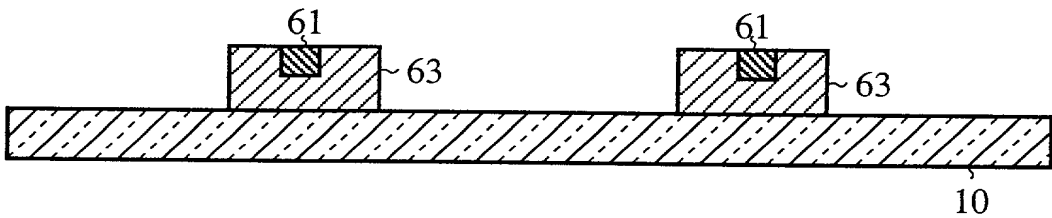


FIG.21C

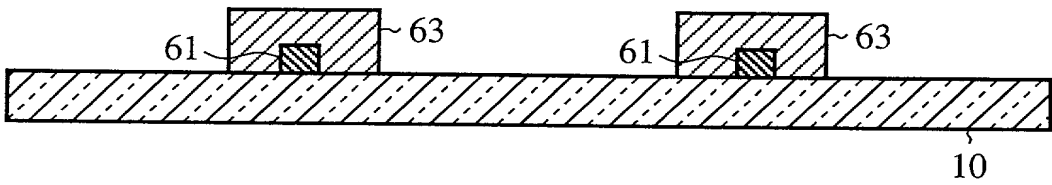


FIG.21D

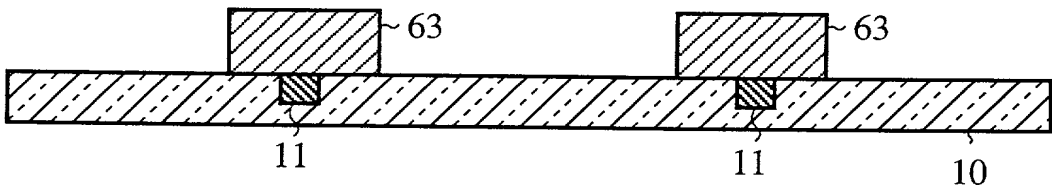




FIG.21E

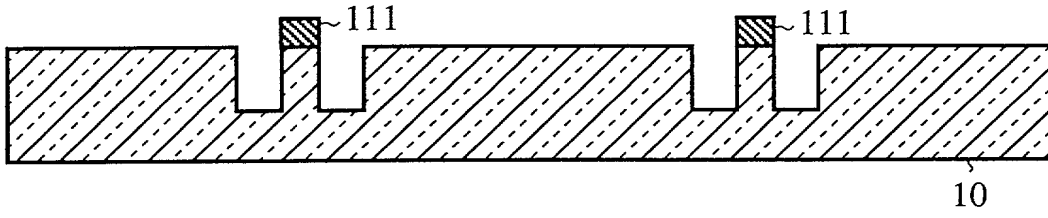


FIG.21F

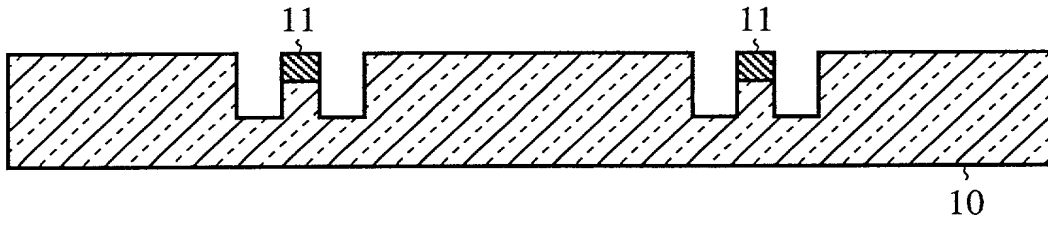


FIG.21G

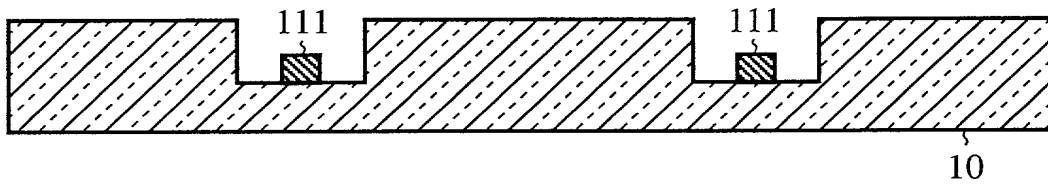


FIG.21H

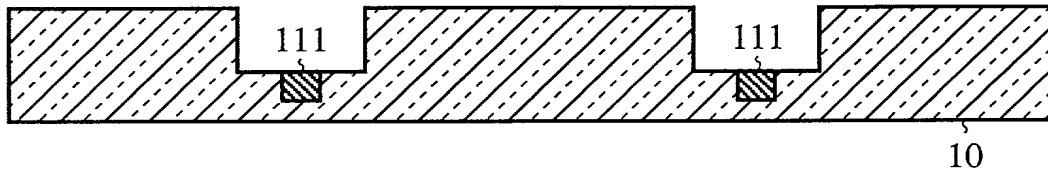


FIG.22A

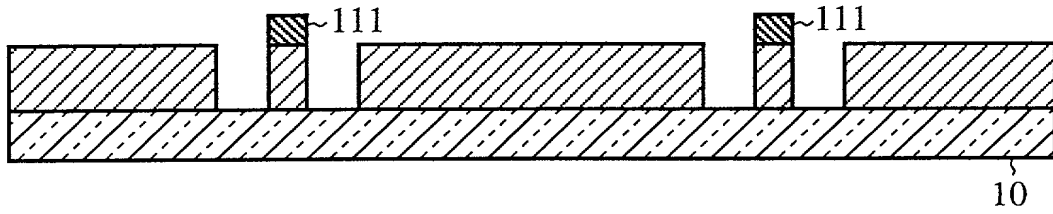


FIG.22B

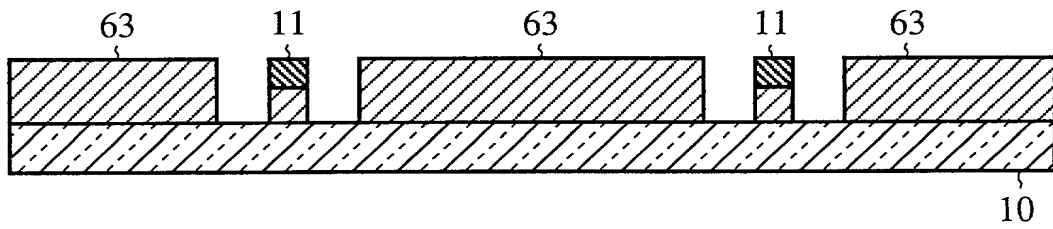


FIG.22C

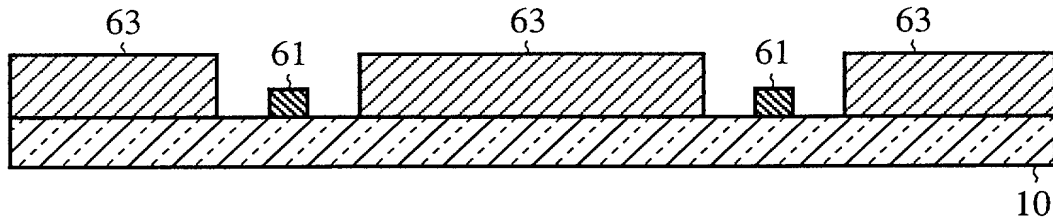


FIG.22D

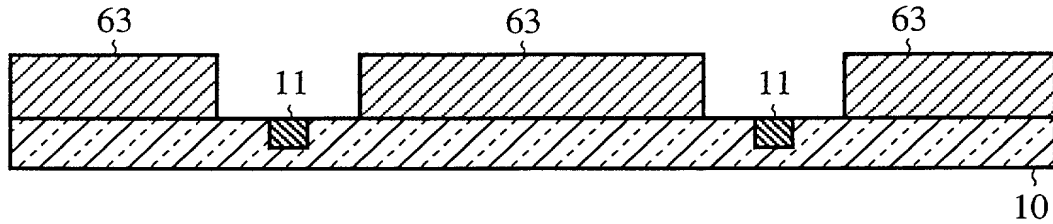


FIG.22E

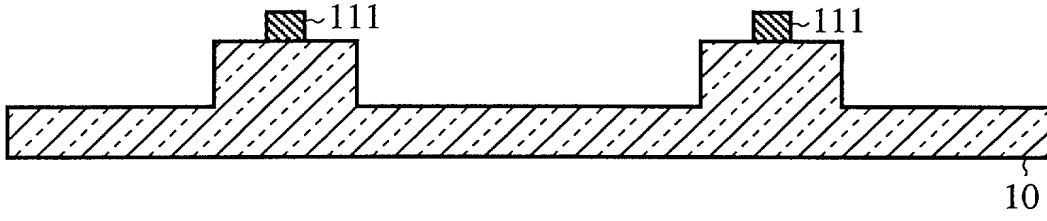


FIG.22F

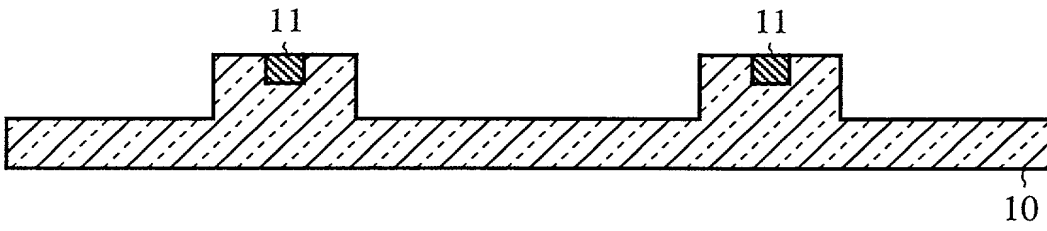


FIG.22G

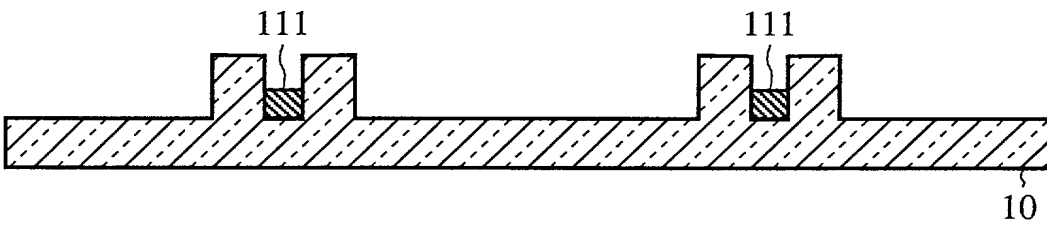


FIG.22H

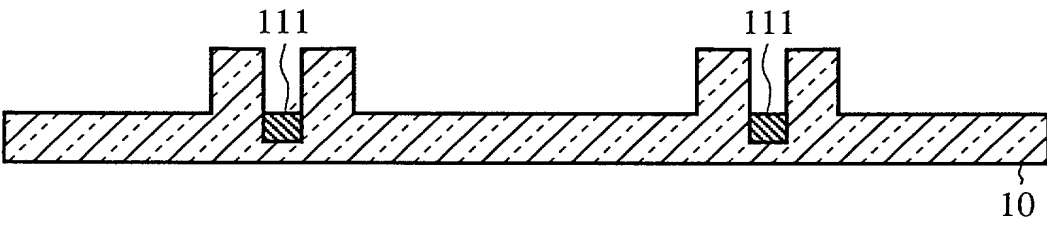


Figure 1 is a cross-sectional view of a semiconductor device. It shows a substrate 10 with a first layer 61 and a second layer 63. A curved surface 70 is formed on the top of the second layer 63.

A cross-sectional view of a semiconductor device. It shows a substrate 10 with a dashed pattern. A gate insulating layer 61 is formed on the substrate. A gate electrode 63 is formed on the gate insulating layer. A channel region 70 is formed in the substrate under the gate electrode. The channel region 70 is shown as a curved region.

FIG.23E

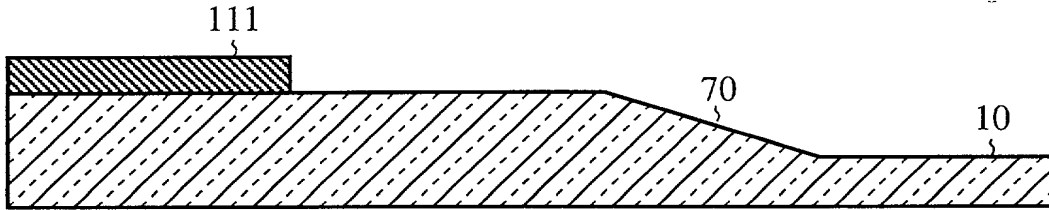


FIG.23F

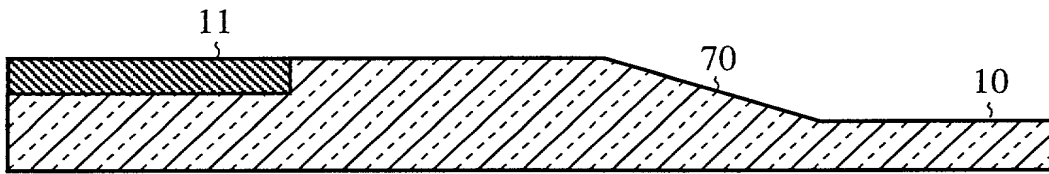


FIG.23G

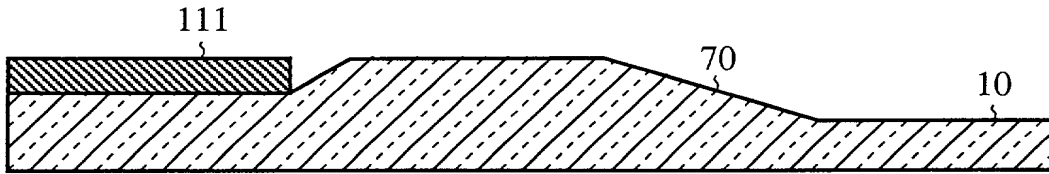


FIG.23H

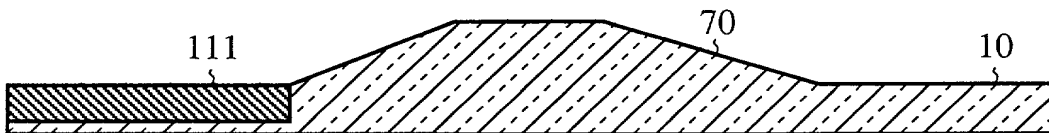
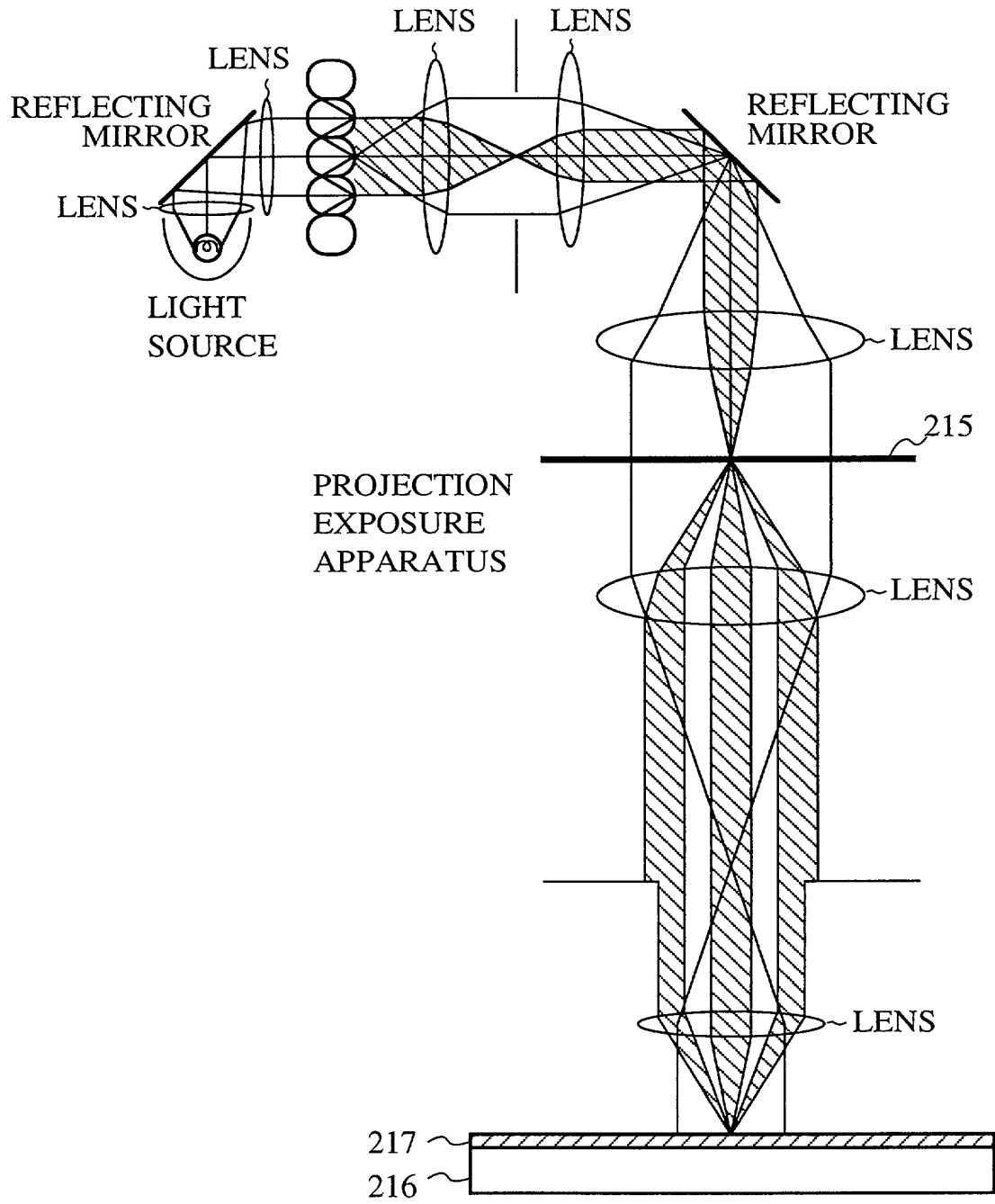


FIG.24



# Declaration and Power of Attorney For Patent Application

## 特許出願宣言書

### Japanese Language Declaration

私は、下欄に氏名を記載した発明者として、以下のとおり宣言する：

私の住所、郵便の宛先および国籍は、下欄に氏名に続いて記載したとおりであり、

名称の発明に関し、請求の範囲に記載した特許を求める主題の本来の、最初にして唯一の発明者である（一人の氏名のみが下欄に記載されている場合）か、もしくは本来の、最初にして共同の発明者である（複数の氏名が下欄に記載されている場合）と信じ、

その明細書を  
(該当する方に印を付す)

☐ ここに添付する。

☐ \_\_\_\_\_ 日に出願番号

第 \_\_\_\_\_ 号として提出し、

\_\_\_\_\_ 日に補正した。  
(該当する場合)

私は、前記のとおり補正した請求の範囲を含む前記明細書の内容を検討し、理解したことを陳述する。

私は、連邦規則法典第37部第1章第56条(a)項に従い、本願の審査に所要の情報を開示すべき義務を有することを認める。

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

"PHOTOMASK, FABRICATION METHOD OF

PHOTOMASK, AND FABRICATION METHOD OF

SEMICONDUCTOR INTEGRATED CIRCUIT"

the specification of which

(check one)

☒ is attached hereto.

☐ was filed on \_\_\_\_\_ as

Application Serial No. \_\_\_\_\_

and was amended on \_\_\_\_\_  
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56

## Japanese Language Declaration

私は、合衆国法典第35部第119条にもとづく下記の外国特許出願または発明者証出願の外国優先権利益を主張し、さらに優先権の主張に係わる基礎出願の出願日前の出願日を有する外国特許出願または発明者証出願を以下に明記する：

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior foreign applications

先の外国出願

Priority claimed

優先権の主張

|          |           |                        |
|----------|-----------|------------------------|
| 11-6956  | Japan     | 13/January/1999        |
| (Number) | (Country) | (Day/Month/Year Filed) |
| (番号)     | (国名)      | (出願の年月日)               |
| <hr/>    |           |                        |
| (Number) | (Country) | (Day/Month/Year Filed) |
| (番号)     | (国名)      | (出願の年月日)               |
| <hr/>    |           |                        |
| (Number) | (Country) | (Day/Month/Year Filed) |
| (番号)     | (国名)      | (出願の年月日)               |

|                                     |                              |
|-------------------------------------|------------------------------|
| <input checked="" type="checkbox"/> | <input type="checkbox"/>     |
| Yes                                 | No                           |
| あり                                  | なし                           |
| <br><input type="checkbox"/>        | <br><input type="checkbox"/> |
| Yes                                 | No                           |
| あり                                  | なし                           |
| <br><input type="checkbox"/>        | <br><input type="checkbox"/> |
| Yes                                 | No                           |
| あり                                  | なし                           |

私は、合衆国法典第35部第120条にもとづく下記の合衆国特許出願の利益を主張し、本願の請求の範囲各項に記載の主題が合衆国法典第35部第112条第1項に規定の態様で先の合衆国出願に開示されていない限度において、先の出願の出願日と本願の国内出願日またはPCT国際出願日の間に公表された連邦規則法典第37部第1章第56条(a)項に記載の所要の情報を開示すべき義務を有することを認める：

I hereby claim the benefit under Title 35, United States Code, §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

|                          |               |
|--------------------------|---------------|
| (Application Serial No.) | (Filing Date) |
| (出願番号)                   | (出願日)         |
| <hr/>                    |               |
| (Application Serial No.) | (Filing Date) |
| (出願番号)                   | (出願日)         |

|                 |                                |
|-----------------|--------------------------------|
| (現況)            | (Status)                       |
| (特許済み、係属中、放棄済み) | (patented, pending, abandoned) |
| <hr/>           |                                |
| (現況)            | (Status)                       |
| (特許済み、係属中、放棄済み) | (patented, pending, abandoned) |

私は、ここに自己の知識にもとづいて行った陳述がすべて真実であり、自己の有する情報および信ずるところに従って行った陳述が真実であると信じ、さらに故意に虚偽の陳述等を行った場合、合衆国法典第18部第1001条により、罰金もしくは禁錮に処せられるか、またはこれらの刑が併科され、またかかる故意による虚偽の陳述が本願ないし本願に対して付与される特許の有効性を損うことがあることを認識して、以上の陳述を行ったことを宣言する。

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.



# Japanese Language Declaration

委任状：私は、下記発明者として、以下の代理人をここに選任し、本願の手続を遂行すること並びにこれに関する一切の行為を特許商標庁に対して行うことを委任する。  
(代理人氏名および登録番号を明記のこと)

POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. (list name and registration number)

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|---------------------------|--------|------------------------|--------|----------------------|--------|
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| Robert S. Swecker         | 19,885 | George A. Hovanec, Jr. | 28,223 | Anthony W. Shaw      | 30,104 |
| Platon N. Mandros         | 22,124 | James A. LaBarre       | 28,632 | Patrick C. Keane     | 32,858 |
| Benton S. Duffett, Jr.    | 22,030 | E. Joseph Gess         | 28,510 | Bruce J. Boggs, Jr.  | 32,344 |
| Joseph R. Magnone         | 24,239 | R. Danny Huntington    | 27,903 | William H. Benz      | 25,952 |
| Norman H. Stepno          | 22,716 | Eric H. Weisblatt      | 30,505 | Peter K. Skiff       | 31,917 |
| Ronald L. Grudziecki      | 24,970 | James W. Peterson      | 26,057 | Richard J. McGrath   | 29,195 |
| Frederick G. Michaud, Jr. | 26,003 | Teresa Stanek Rea      | 30,427 | Matthew L. Schneider | 32,814 |
| Alan E. Kopecki           | 25,813 | Robert E. Krebs        | 25,885 | Michael G. Savage    | 32,596 |
| Regis E. Slutter          | 26,999 | Robert M. Schulman     | 31,196 | Gerald F. Swiss      | 30,113 |
| Samuel C. Miller, III     | 27,360 |                        |        |                      |        |

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Platon N. Mandros  
703/836-6620

|                      |  |                                  |
|----------------------|--|----------------------------------|
| 唯一のまたは第一の発明者の氏名      | Full name of sole or first inventor  |                                  |
|                      | Kazuya KAMON   |                                  |
| 同発明者の署名              | 日付   | Inventor's signature Date        |
|                      |  | Kazuya Kamon April 27, 1999      |
| 住所                   | Residence  |                                  |
|                      | Tokyo, Japan   |                                  |
| 国籍                   | Citizenship  |                                  |
|                      | Japan  |                                  |
| 郵便の宛先                | Post Office Address  |                                  |
|                      | c/o MITSUBISHI DENKI KABUSHIKI KAISHA,<br>2-3, Marunouchi 2-chome, Chiyoda-ku,<br>Tokyo 100-8310 Japan |                                  |
| 第2の共同発明者の氏名 (該当する場合) | Full name of second joint inventor, if any   |                                  |
| 同第2発明者の署名            | 日付   | Second Inventor's signature Date |
|                      |  |                                  |
| 住所                   | Residence  |                                  |
|                      |  |                                  |
| 国籍                   | Citizenship  |                                  |
|                      |  |                                  |
| 郵便の宛先                | Post Office Address  |                                  |
|                      |  |                                  |

(第六またはそれ以降の共同発明者に対しても同様な情報および署名を提供すること。)

(Supply similar information and signature for third and subsequent joint inventors.)